



(12) **EUROPEAN PATENT APPLICATION**

(43) Date of publication:
16.08.2000 Bulletin 2000/33

(51) Int. Cl.⁷: **H04L 27/38**

(21) Application number: **00102770.5**

(22) Date of filing: **10.02.2000**

(84) Designated Contracting States:
AT BE CH CY DE DK ES FI FR GB GR IE IT LI LU
MC NL PT SE
 Designated Extension States:
AL LT LV MK RO SI

(30) Priority: **12.02.1999 JP 3403899**
19.02.1999 JP 4122799
23.02.1999 JP 4439099

(71) Applicant:
MATSUSHITA ELECTRIC INDUSTRIAL CO., LTD.
Kadoma-shi, Osaka 571-8501 (JP)

(72) Inventors:
 • **Murakami, Yutaka**
Yokohama-shi, Kanagawa 230-0074 (JP)
 • **Takabayashi, Shinichiro**
Kawasaki-shi, Kanagawa 216-0015 (JP)
 • **Orihashi, Masayuki**
Ichikawa-shi, Chiba 272-0001 (JP)
 • **Matsuoka, Akihiko**
Yokohama-shi, Kanagawa 226-0021 (JP)

(74) Representative:
Grünecker, Kinkeldey,
Stockmalr & Schwanhäusser
Anwaltssozietät
Maximilianstrasse 58
80538 München (DE)

(54) **Distortion estimation apparatus, frequency offset compensation apparatus and reception apparatus for QAM signals**

(57) In-phase component approximation section 111 calculates an amplitude variation amount of an in-phase component of a data symbol from an amplitude variation amount of an in-phase component of a pilot symbol approximately. Quadrature component approximation section 112 calculates an amplitude variation amount of a quadrature component of the data symbol from an amplitude variation amount of a quadrature component of the pilot symbol approximately. Amplitude variation calculation section 113 calculates an amplitude variation amount of each data symbol from the amplitude variation amounts of the in-phase component and quadrature component of each data symbol. Phase variation calculation section 114 calculates a phase variation amount of the data symbol from a phase variation amount of the pilot symbol approximately. Thus, by calculating the amplitude variation amount of the data symbol and the phase variation amount of the data symbol separately, even in the case where a frequency offset between a transmitter and receiver is large, it is possible to estimate the frequency offset and fading distortion with high accuracy, and to suppress the deterioration of bit error rate characteristic.

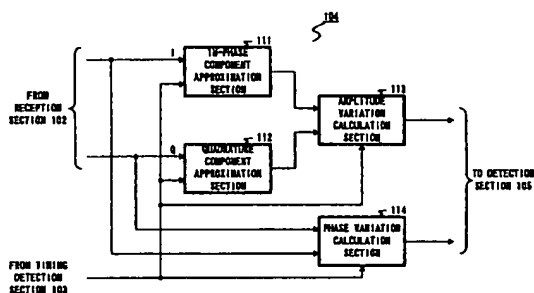


FIG. 5

Description

BACKGROUND OF THE INVENTION

Field of the Invention

[0001] The present invention relates to a distortion estimation apparatus, frequency offset compensation apparatus, reception apparatus and their method used in a radio communication apparatus.

Description of the Related Art

[0002] In radio communications for which the demand has been increased rapidly in recent years, the improvement of spectral efficiency is required. One of effective technologies for improving the spectral efficiency is a QAM (Quadrature Amplitude Modulation).

[0003] In order to apply the QAM to the radio communication, it is necessary to compensate a fading distortion of a transmission signal generated under the Rayleigh fading environment.

[0004] As a method, conventionally known, for compensating such a fading distortion, there is, for example, "Rayleigh Fading Compensation Method for 16QAM MODEM in Digital Land Mobile Radio Systems" (SAM-PEI, Technical Report of IEICE B-II, Vol.J-72-B-II, No.1, pp.7-15, January, 1989).

[0005] This method relates a system in which a transmission side transmits transmission data with a frame structure comprised of the predetermined number of symbols with a pilot symbol, known between the transmission and reception sides, inserted between data symbol sequences periodically, and the reception side detects a signal space position of the pilot symbol, and estimates a fading distortion amount of each symbol from the relationship between the relative position of each point to compensate.

[0006] This method uses the characteristic of the radio communication that the scale and gradient of a signal space diagram varies depending on an amplitude variation and phase variation of the transmission signal generated under the rayleigh fading environment, however the entire shape of the signal space diagram does not vary.

[0007] An example of conventional technologies using this system is explained below using accompanying drawings.

[0008] FIG. 1 is a block diagram illustrating a configuration of a conventional distortion compensation apparatus.

[0009] The distortion compensation apparatus in FIG.1 is mainly comprised of clock reproduction section 11, pilot detection section 12, pilot symbol extraction section 13, distortion amount calculation section 14, data symbol extraction section 15, data delay section 16 and distortion estimation compensation section 17.

[0010] In the distortion compensation apparatus in

FIG.1, a received complex baseband signal $u(t)$ is inputted to clock reproduction section 11, pilot detection section 12, pilot symbol extraction section 13, and data symbol extraction section 15.

[0011] Clock reproduction section 11 controls a clock signal oscillated from an internal oscillator based on the received complex baseband signal $u(t)$, and outputs the controlled clock signal to pilot detection section 12 and data symbol extraction section 15.

[0012] Pilot detection section 12 detects a position of the pilot symbol in the received complex baseband signal $u(t)$ based on the clock signal, and outputs a signal indicative of the position of the pilot symbol to pilot symbol extraction section 13.

[0013] Pilot symbol extraction section 13 extracts the pilot symbol from the received complex baseband signal $u(t)$ at the same timing when a signal is inputted from pilot detection section 12, and outputs the extracted pilot symbol to distortion amount calculation section 14.

[0014] Herein, when it is assumed that a transmission complex baseband signal is $z(t)$, and that a fading distortion is $c(t)$, the received complex baseband signal $u(t)$ can be expressed with the equation (1) shown below.

$$U(t)=z(t) \times c(t) \quad (1)$$

[0015] Further, when it is assumed that a received pilot symbol is u_p , and that a transmission pilot symbol is t_p , a fading distortion amount c_p of the pilot symbol can be obtained with the equation (2) shown below.

$$c_p = u_p / t_p \quad (2)$$

[0016] Distortion amount calculation section 14 calculates fading distortion amount c_p of the pilot symbol with the above equation (2) to output to distortion estimation compensation section 17.

[0017] Data symbol extraction section 15 extracts a data symbol from the received complex baseband signal $u(t)$ based on the clock signal to output to data delay section 16.

[0018] Data delay section 16 delays the inputted data symbol by a time required in distortion estimation compensation section 17 for estimating the fading distortion amount of the data symbol to output to fading distortion estimation compensation section 17.

[0019] Distortion estimation compensation section 17 interpolates the fading distortion amount of the data symbol on the time axis based on fading distortion amount c_p of the pilot symbol to estimate. Then, distortion estimation compensation section 17 multiplies the data symbol by a reciprocal (complex conjugate) of the fading distortion amount of the data symbol to compensate the fading distortion of the data symbol.

[0020] FIG.2 is a diagram showing a relationship between time (abscissa) and distortion amount of each

symbol (ordinate). As illustrated in FIG.2, approximate polynomial 21 is determined based on the fading distortion amount of the pilot symbol (○), and fading distortion amount of each data symbol (●) is placed on approximate polynomial 21. In addition, FIG.2 adopts the frame structure in which the pilot symbol is inserted periodically every 8 symbols.

[0021] Thus, the above-described conventional distortion apparatus performs the interpolation based on the fading distortion amount of the pilot symbol, and thereby estimates the fading distortion amount of the data symbol to compensate.

[0022] As the causes of the distortion generated in the transmission signal in radio communications, there is a frequency offset between a transmitter and receiver besides the fading.

[0023] However, in the above-described conventional distortion compensation apparatus, since the frequency offset is not considered, the estimation accuracy lowers in the case where the frequency offset is large, resulting in the problem that the bit error rate characteristic deteriorates.

SUMMARY OF THE INVENTION

[0024] A first object of the present invention is to estimate a frequency offset and fading distortion with high accuracy even in the case where the frequency offset between a transmitter and receiver is large, and to suppress the deterioration of the bit error rate characteristic.

[0025] The above object of the present invention is achieved by calculating an amplitude variation amount of data symbol and phase variation amount of the data symbol separately, or estimating a fading distortion of the data symbol using the pilot symbol subjected to frequency offset compensation.

[0026] Further, a second object of the present invention is to obtain excellent bit error rate characteristic even in the case where the fading variation is large, and a signal with low received field strength is received.

[0027] The above object of the present invention is achieved by judging whether or not the pilot symbol was detected effectively, and calculating the frequency offset using only the effectively detected pilot symbol.

BRIEF DESCRIPTION OF THE DRAWINGS

[0028] The above and other objects and features of the invention will appear more fully hereinafter from a consideration of the following description taken in connection with the accompanying drawing wherein one example is illustrated by way of example, in which;

FIG.1 is a block diagram illustrating a configuration of a conventional distortion compensation apparatus;

FIG.2 is a diagram illustrating a concept of an inter-

polation to perform a conventional distortion compensation;

FIG.3 is a diagram illustrating a frame structure of a received signal received at a reception apparatus according to each embodiment of the present invention;

FIG.4 is a block diagram illustrating a configuration of a reception apparatus according to a first embodiment of the present invention;

FIG.5 is a block diagram illustrating a configuration of a distortion estimation apparatus according to the first embodiment of the present invention;

FIG.6 is a block diagram illustrating a configuration of a reception apparatus according to a second embodiment of the present invention;

FIG.7 is a conceptual diagram illustrating a time change of a phase of a pilot symbol in a frequency offset compensation;

FIG.8 is a diagram illustrating a configuration of a frequency offset compensation apparatus according to a second embodiment;

FIG.9 is a block diagram illustrating a configuration of a distortion estimation apparatus according to the second embodiment of the present invention;

FIG.10 is a conceptual diagram illustrating a relationship between received power and an estimated error of a phase variation amount;

FIG.11 is a block diagram illustrating a configuration of a reception apparatus according to a third embodiment of the present invention;

FIG.12 is a diagram illustrating a configuration of a frequency offset compensation apparatus according to the third embodiment;

FIG.13 is a block diagram illustrating a configuration of a reception apparatus according to a fourth embodiment of the present invention;

FIG.14 is a block diagram illustrating a configuration of a reception apparatus according to a fifth embodiment of the present invention;

FIG.15 is a diagram illustrating a configuration of a frequency offset compensation apparatus according to the fifth embodiment;

FIG.16 is a block diagram illustrating a configuration of a distortion estimation apparatus according to a fifth embodiment of the present invention;

FIG.17 is a diagram illustrating a configuration of a reception apparatus according to a sixth embodiment;

FIG.18 is a block diagram illustrating a configuration of a frequency offset compensation apparatus according to the sixth embodiment of the present invention; and

FIG.19 is a block diagram illustrating a configuration of a reception apparatus according to a seventh embodiment of the present invention.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

[0029] Embodiments of the present invention will be described specifically below with reference to drawings. In addition, in the following explanation, a single carrier system is used as a communication system. Further, in the following explanation, it is assumed that as illustrated in FIG.3, in the frame structure of a received signal, 1 block has 8 symbols, and the first 1 symbol of each block is a pilot symbol. Furthermore, it is assumed that as a polynomial approximation for use in an interpolation, the first-order Gaussian polynomial approximation is used.

(First embodiment)

[0030] FIG.4 is a block diagram illustrating a configuration of a reception apparatus according to the first embodiment of the present invention. The reception apparatus illustrated in FIG.4 is mainly comprised of antenna 101, reception radio section 102, timing detection section 103, distortion estimation section 104 according to the first embodiment, and detection section 105.

[0031] In the reception apparatus illustrated in FIG.4, a signal received at antenna 101 is inputted to reception radio section 102. Reception radio section 102 executes quadrature demodulation on the inputted signal, and outputs an in-phase component and quadrature component of a received quadrature baseband signal to timing detection section 103, distortion estimation apparatus 104 and detection section 105.

[0032] Timing detection section 103 acquires frame-symbol synchronization based on the in-phase component and quadrature component of the received quadrature baseband signal, and outputs a timing signal indicative of a head of a frame-symbol to distortion estimation apparatus 104 and detection section 105.

[0033] Distortion estimation apparatus 104 receives the inputted in-phase component and quadrature component of the received quadrature baseband signal and timing signal, estimates an amplitude variation amount and phase variation amount as described later, and outputs the estimated results to detection section 105.

[0034] Detection section 105 executes detection processing on the in-phase component and quadrature component of the received quadrature baseband signal using the amplitude variation amount and phase variation amount, and outputs a digital signal. In addition, as a method for using the amplitude variation amount and phase variation amount, for example, there are a method for varying a threshold for use in judgment in the detection processing based on the amplitude variation amount and phase variation amount, and another method for multiplying the in-phase component and quadrature component of the received quadrature baseband signal by inverse properties of the amplitude

variation amount and phase variation amount.

[0035] FIG.5 is a block diagram illustrating an internal configuration of a distortion estimation apparatus according to this embodiment. Distortion estimation apparatus 104 illustrated in FIG.5 is mainly comprised of in-phase component approximation section 111, quadrature component approximation section 112, amplitude variation calculation section 113, and phase variation calculation section 114.

[0036] In-phase component approximation section 111 receives the inputted in-phase component of the received quadrature baseband signal and timing signal, and calculates the amplitude variation amount of the in-phase component of the data symbol from that of the in-phase component of the pilot symbol approximately. For example, in FIG.3, amplitude variation amount r_k of the in-phase component of data symbol k ($k=1$ to 7) of block j is calculated from in-phase component I_i of pilot symbol i and in-phase component I_{i-1} of pilot symbol $i-1$ with the equation (3) shown below.

$$r_k = k \times \frac{I_i}{8} + (1 - \frac{k}{8}) \times I_{i-1} \quad \Delta \quad (3)$$

[0037] Then, in-phase component approximation section 111 outputs a signal indicative of amplitude variation amount r_k of the in-phase component of the data symbol calculated with the above equation (3) to amplitude variation calculation section 113.

[0038] Quadrature component approximation section 112 receives the inputted quadrature component of the received quadrature baseband signal and timing signal, and calculates the amplitude variation amount of the quadrature component of the data symbol from that of the quadrature component of the pilot symbol approximately. For example, in FIG.3, amplitude variation amount s_k of the quadrature component of data symbol k ($k=1$ to 7) of block j is calculated from quadrature component Q_i of pilot symbol i and quadrature component Q_{i-1} of pilot symbol $i-1$ with the equation (4) shown below.

$$s_k = k \times \frac{Q_i}{8} + (1 - \frac{k}{8}) \times Q_{i-1} \quad \Delta \quad (4)$$

[0039] Then, quadrature component approximation section 112 outputs a signal indicative of amplitude variation amount s_k of the quadrature component of the data symbol calculated with the above equation (4) to amplitude variation calculation section 113.

[0040] Amplitude variation calculation section 113 calculates the amplitude variation amount of each data symbol from the amplitude variation amounts of the in-phase component and quadrature component of each data symbol. For example, in FIG.3, amplitude variation amount A_k of data symbol k ($k=1$ to 7) of block j is calculated

lated using amplitude variation amount r_k of the in-phase component and amplitude variation amount s_k of the quadrature component with the equation (5) shown below.

$$A_k = \sqrt{r_k^2 + s_k^2} \Lambda \quad (5)$$

Then, amplitude variation calculation section 113 outputs a signal indicative of the amplitude variation amount of the data symbol calculated with the above equation (5) to detection section 105.

[0041] Phase variation calculation section 114 receives the inputted in-phase component and quadrature component of the received quadrature baseband signal and timing signal, and calculates the phase variation amount of the data symbol based on the phase variation amount of the pilot symbol approximately. For example, in FIG.3, phase variation amount θ_k of the data symbol k ($k=1$ to 7) of block j is calculated from in-phase component I_i of pilot symbol i , in-phase component I_{i-1} of pilot symbol $i-1$, quadrature component Q_i of pilot symbol i and quadrature component Q_{i-1} of pilot symbol $i-1$ with the equation (6) shown below.

$$\theta_k = \frac{k}{8} \tan^{-1} \left(\frac{Q_i}{I_i} \right) + \left(1 - \frac{k}{8} \right) \tan^{-1} \left(\frac{Q_{i-1}}{I_{i-1}} \right) \Lambda \quad (6)$$

[0042] In addition, phase variation amount θ_k calculated with the above equation (6) is a value including a phase variation amount caused by the fading and another phase variation amount caused by the frequency offset.

[0043] Then, phase variation calculation section 114 outputs a signal indicative of phase variation amount θ_k of the data symbol calculated with the above equation (6) to detection section 105.

[0044] Thus, by calculating the amplitude variation amount of the data symbol and the phase variation amount of the data symbol separately, it is possible to estimate the frequency offset between a transmitter and receiver and the fading distortion both with high accuracy.

[0045] Then, as illustrated in FIG.4, by installing the above-described distortion estimation apparatus in a reception apparatus, since it is possible to estimate the frequency offset between a transmitter and receiver and the fading distortion both with high accuracy to detect, it is possible to suppress the deterioration of the bit error rate characteristic.

(Second embodiment)

[0046] FIG.6 is a block diagram illustrating a configuration of a reception apparatus according to the second embodiment of the present invention. In addition, in the reception apparatus in FIG.6, the components hav-

ing the function common to that in the reception apparatus illustrated in FIG.4 are given the same symbols as in FIG.4, and the explanations thereof are omitted.

[0047] The reception apparatus in FIG.6 adopts a configuration in which frequency offset compensation apparatus 200 is added to the reception apparatus in FIG.4. In addition, in the reception apparatus in FIG.6, the functions of distortion estimation apparatus 202 and detection section 203 are different from those of distortion estimation apparatus 104 and detection section 105 of the reception apparatus in FIG.4.

[0048] FIG.7 is a diagram illustrating a time change of a phase of a pilot symbol on the complex plane. As illustrated in FIG.7, at time $t=mNT$ (m is a natural number, N is an insertion interval of a pilot symbol, and T is a symbol period), when it is assumed that a phase variation amount between a pilot symbol and a previous pilot symbol just before is αm , in the case where the frequency offset is present, each pilot symbol ($P_0, P_1, P_2, \dots, P_{m-1}, P_m, P_{m+1}, \dots$) rotates to a constant direction on the complex plane by a rotation amount proportional to a frequency offset amount.

[0049] Frequency offset compensation apparatus 201 receives the inputted in-phase component and quadrature component of the received quadrature baseband signal and timing signal, and extracts the pilot symbol. Then, frequency offset compensation apparatus 201 calculates the phase variation amount between the pilot symbols to calculate the frequency offset amount, and compensates the frequency offset of the pilot symbol by inversely rotating the phase of the pilot symbol by the calculated frequency offset amount.

[0050] Then, frequency offset compensation apparatus 201 outputs the in-phase component and quadrature component of the pilot symbol subjected to the frequency offset compensation to distortion estimation apparatus 202, and further outputs the estimated frequency offset amount to detection section 203. In addition, the detailed internal configuration of frequency offset compensation apparatus 201 is described later.

[0051] Distortion estimation apparatus 202 receives the inputted in-phase component and quadrature component of the received quadrature baseband signal and timing signal, estimates the amplitude variation amounts of the in-phase component and quadrature component of the pilot symbol as described later, and outputs the estimated results to detection section 203.

[0052] Detection section 203 executes detection processing on the in-phase component and quadrature component of the received quadrature baseband signal using the frequency offset amount and the amplitude variation amount of the pilot symbol, and outputs a digital signal. As a method for using the frequency offset amount and the amplitude variation amount of the pilot symbol, for example, there are a method for varying a threshold for use in judgment in the detection processing based on the frequency offset amount and the amplitude variation amount of the pilot symbol, and

another method for multiplying the in-phase component and quadrature component of the received quadrature baseband signal by inverse properties of the frequency offset amount and the amplitude variation amount of the pilot symbol.

[0053] The internal configuration of frequency offset compensation apparatus 201 of the distortion estimation apparatus according to this embodiment is next explained using a block diagram in FIG.8.

[0054] Frequency offset compensation apparatus 201 is mainly comprised of phase variation amount calculation section 211, memory section 212, frequency offset calculation section 213, and frequency offset removing section 214.

[0055] Phase variation amount calculation section 211 receives the inputted in-phase component and quadrature component of the received quadrature baseband signal and timing signal, extracts the pilot symbol, and obtains phase variation amount θ_i between the pilot symbols from in-phase component I_i of pilot symbol i , in-phase component I_{i-1} of pilot symbol $i-1$, quadrature component Q_i of pilot symbol i and quadrature component Q_{i-1} of pilot symbol $i-1$ with the equation (7) shown below. Then, phase variation amount calculation section 211 outputs a signal indicative of phase variation amount θ_i to memory section 212 and frequency offset calculation section 213.

$$\theta_k = \frac{k}{8} \tan^{-1} \left(\frac{Q_i}{I_i} \right) + \left(1 - \frac{k}{8} \right) \tan^{-1} \left(\frac{Q_{i-1}}{I_{i-1}} \right) \quad (7)$$

[0056] Memory section 212 holds the inputted phase variation amount θ_i , and according to the timing signal, outputs phase variation amount θ_{i-1} already held.

[0057] Frequency offset calculation section 213 calculates the frequency offset amount using, for example, phase variation amount θ_i and phase variation amount θ_{i-1} , and outputs a signal indicative of the frequency offset amount to detection section 203 and frequency offset removing section 214. In addition, the frequency offset amount is generally obtained by averaging a plurality of phase variation amounts obtained previously.

[0058] Frequency offset removing section 214 receives the inputted in-phase component and quadrature component of the received quadrature baseband signal, timing signal and the signal indicative of the frequency offset amount, extracts the pilot symbol, removes the frequency offset amount from the in-phase component and quadrature component of the pilot symbol to compensate, and outputs the in-phase component and quadrature component of the pilot symbol subjected to the frequency offset compensation to distortion estimation apparatus 202.

[0059] The internal configuration of distortion estimation apparatus 202 according to this embodiment is next explained using a block diagram in FIG.9.

[0060] Distortion estimation apparatus 202 is mainly comprised of in-phase component approximation section 221, and quadrature component approximation section 222.

[0061] In-phase component approximation section 221 receives the inputted in-phase component of the pilot symbol subjected to the frequency offset compensation and timing signal, and calculates the fading distortion amount of the in-phase component of the data symbol approximately. For example, in FIG.3 previously described, fading distortion amount r_k of the in-phase component of symbol k ($k=1$ to 7) of block j of data symbols is calculated from in-phase component I_i of pilot symbol i and in-phase component I_{i-1} of pilot symbol $i-1$ with the equation (8) shown below.

$$r_k = k \times \frac{I_i}{8} + \left(1 - \frac{k}{8} \right) \times I_{i-1} \quad (8)$$

[0062] Then, in-phase component approximation section 221 outputs a signal indicative of fading distortion amount r_k of the in-phase component of the data symbol calculated to detection section 203.

[0063] Quadrature component approximation section 222 receives the inputted quadrature component of the pilot symbol subjected to the frequency offset compensation and timing signal, and calculates the fading distortion amount of the quadrature component of the data symbol approximately. For example, in FIG.3, fading distortion amount s_k of the quadrature component of symbol k ($k=1$ to 7) of block j of data symbols is calculated from quadrature component Q_i of pilot symbol i and quadrature component Q_{i-1} of pilot symbol $i-1$ with the equation (9) shown below.

$$s_k = k \times \frac{Q_i}{8} + \left(1 - \frac{k}{8} \right) \times Q_{i-1} \quad (9)$$

[0064] Then, quadrature component approximation section 222 outputs a signal indicative of fading distortion amount s_k of the quadrature component of the data symbol calculated to detection section 203.

[0065] Thus, by compensating the frequency offset of the pilot symbol, and estimating the fading distortion of the data symbol using the pilot symbol subjected to the frequency offset compensation, it is possible to estimate the frequency offset between a transmitter and receiver and the fading distortion with high accuracy.

[0066] Then, as illustrated in FIG.6, by installing the above-described frequency offset compensation apparatus and distortion estimation apparatus in a reception apparatus, since it is possible to estimate the frequency offset between a transmitter and receiver and the fading distortion both with high accuracy to detect, it is possible to suppress the deterioration of the bit error rate characteristic.

(Third embodiment)

[0067] When the received field strength is low due to, for example, fading, the estimated error of phase variation amount becomes large because effects of noise are increased.

[0068] For example, in FIG.10, it is assumed that signal A with high received field strength and signal B with low received field strength each moves in a range of a circle with broken lines due to the effects of noise. As can be seen from FIG.10, error θ_b of phase rotation angle relating to signal B is larger than error θ_a of phase rotation angle relating to signal A. Further, with respect to signal C with extremely low received field strength, by the effects of noise, the quadrant on which signal C is present changes, and therefore it is not possible to estimate the phase rotation angle correctly.

[0069] When the frequency offset amount is estimated using a signal with low field strength, the bit error rate characteristic deteriorates because the estimated error becomes large.

[0070] The third embodiment of the present invention is obtained to solve the above problem, and in this embodiment, excellent bit error rate characteristic can be obtained even in the case where the fading variation is large, and a signal with low field strength is received.

[0071] FIG.11 is a block diagram illustrating a configuration of a reception apparatus according to the third embodiment of the present invention. In addition, in the reception apparatus in FIG.11, the components having the function common to that in the reception apparatus illustrated in FIG.6 are given the same symbols as in FIG.6, and the explanations thereof are omitted.

[0072] In the reception apparatus in FIG.11, the functions of frequency offset compensation apparatus 301 and detection section 302 are different from those of frequency offset compensation apparatus 201 and detection section 203 of the reception apparatus illustrated in FIG.6. In addition, the reception apparatus in FIG.11 adopts a configuration in which distortion estimation apparatus 202 is eliminated as compared the reception apparatus illustrated in FIG.6.

[0073] Frequency offset compensation apparatus 301 receives the inputted in-phase component and quadrature component of the received quadrature baseband signal and timing signal, and extracts the pilot symbol. Then, frequency offset compensation apparatus 301 judges whether or not the pilot symbol was detected effectively, calculates a frequency offset amount using only the effectively detected pilot symbol, and compensates the frequency offset amount of the pilot symbol by inversely rotating the phase of the received quadrature baseband signal by the calculated frequency offset amount.

[0074] Then, frequency offset compensation apparatus 301 outputs the in-phase component and quadrature component of the pilot symbol subjected to the frequency offset compensation and estimated fre-

quency offset amount to detection section 302.

[0075] Detection section 302 executes detection processing on the in-phase component and quadrature component of the received quadrature baseband signal using the frequency offset amount, and outputs a digital signal. As a method for using the frequency offset amount, for example, there are a method for varying a threshold for use in judgment in the detection processing based on the frequency offset amount, and another method for multiplying the in-phase component and quadrature component of the received quadrature baseband signal by an inverse property of the frequency offset amount.

[0076] FIG.12 is a block diagram illustrating an internal configuration of frequency offset compensation apparatus 301. In frequency offset compensation apparatus 301 in FIG.12, the function of frequency offset amount calculation section 311 is different from that of frequency offset amount calculation section 213 in frequency offset compensation apparatus 201 illustrated in FIG.8. Further, frequency offset compensation apparatus 301 in FIG.12 adopts a configuration in which correlation value calculation section 312 is added as compared to frequency offset compensation apparatus 201 illustrated in FIG.8.

[0077] Correlation value calculation section 312 receives phase variation amount θ_i inputted from phase variation calculation amount section 211 and phase variation amount θ_{i-1} inputted from memory section 212, and calculates the correlation value indicative of the correlation between phase variation amount θ_i and phase variation amount θ_{i-1} (for example, an absolute value of a value obtained by subtracting phase variation amount θ_{i-1} from phase variation amount θ_i) to output to frequency offset amount calculation section 311.

[0078] Based on the correlation value, frequency offset amount calculation section 311 judges whether or not there is the correlation between phase variation amount θ_i and phase variation amount θ_{i-1} , in other words, whether or not pilot symbol i was effectively detected. For example, in the case where the correlation value is equal to or less than a predetermined threshold, it is judged that there is the correlation between phase variation amount θ_i and phase variation amount θ_{i-1} , and that pilot symbol i was effectively detected.

[0079] Then, frequency offset amount calculation section 311 calculates the frequency offset amount using phase variation amount θ_i when judges that there is the correlation between phase variation amount θ_i and phase variation amount θ_{i-1} , while calculating the frequency offset amount without using phase variation amount θ_i when judges that there is not the correlation between phase variation amount θ_i and phase variation amount θ_{i-1} .

[0080] In addition, it may be possible that frequency offset amount calculation section 311 outputs a signal indicative of a judgement result on whether or not there

is the correlation between phase variation amount θ_i and phase variation amount θ_{i-1} (hereinafter referred to as "correlation result signal") to an external apparatus such as a CPU not shown in the figure. In this case, based on the content of the correlation result signal, the external apparatus may perform an judgement of data effectiveness and judgment of channel switch such as diversity and handover.

[0081] Further, it may be possible that correlation value calculation section 312 outputs a signal indicative of the correlation value between phase variation amount θ_i and phase variation amount θ_{i-1} (hereinafter referred to as "correlation value signal") to detection section 203, and that detection section 203 judges whether or not there is the correlation between phase variation amount θ_i and phase variation amount θ_{i-1} .

[0082] Furthermore, it may be possible that correlation value calculation section 312 outputs the correlation value signal to an external apparatus such as a CPU not shown in the figure, and that the external apparatus such as the CPU judges whether or not there is the correlation between phase variation amount θ_i and phase variation amount θ_{i-1} , and based on the judgement result, performs an judgement of data effectiveness and judgment of channel switch such as diversity and handover. In this case, phase variation amount calculation section 211, memory section 212 and correlation value calculation section 312 function as the control apparatus to calculate the correlation value.

[0083] Thus, by judging whether or not the pilot symbol was detected effectively, and calculating the frequency offset amount of the pilot symbol using only the effectively detected pilot symbol, it is possible to obtain excellent bit error rate characteristic even in the case where the fading variation is large, and the signal with low received field strength is received.

(Fourth embodiment)

[0084] The fourth embodiment of the present invention is obtained by combining the second embodiment and third embodiment, and in this embodiment in the case where a reception apparatus has a frequency offset compensation apparatus and distortion estimation apparatus installed therein, excellent bit error rate characteristic can be obtained even in the case where a fading variation is large, and a signal with low field strength is received.

[0085] FIG.13 is a block diagram illustrating a configuration of a reception apparatus according to the fourth embodiment of the present invention. In addition, in the reception apparatus in FIG.13, the components having the function common to that in the reception apparatus illustrated in FIG.6 are given the same symbols as in FIG.6, and the explanations thereof are omitted.

[0086] The reception apparatus in FIG.13 has frequency offset compensation apparatus 301 illustrated in

FIG.11 installed therein, instead of frequency offset compensation apparatus 201, as compared to the reception apparatus illustrated in FIG.6.

[0087] Frequency offset compensation apparatus 301 receives the inputted in-phase component and quadrature component of the received quadrature baseband signal and timing signal, and extracts the pilot symbol. Then, frequency offset compensation apparatus 301 judges whether or not the pilot symbol was detected effectively, calculates a frequency offset amount using only the effectively detected pilot symbol, and compensates the frequency offset amount of the pilot symbol by inversely rotating the phase of the received quadrature baseband signal by the calculated frequency offset amount.

[0088] Then, frequency offset compensation apparatus 301 outputs the in-phase component and quadrature component of the pilot symbol subjected to the frequency offset compensation to distortion estimation apparatus 202, and further outputs the estimated frequency offset amount to detection section 203.

[0089] Detection section 203 executes detection processing on the in-phase component and quadrature component of the received quadrature baseband signal using the frequency offset amount and the amplitude variation amount of the pilot symbol, and outputs a digital signal.

[0090] Thus, by judging whether or not the pilot symbol was detected effectively, calculating the frequency offset amount of the pilot symbol using only the effectively detected pilot symbol to compensate the frequency offset of the pilot symbol, and estimating the fading distortion of the data symbol using the pilot symbol subjected to the frequency offset compensation, it is possible to obtain excellent bit error rate characteristic even in the case where the fading variation is large, and the signal with low received field strength is received, and further to estimate the frequency offset between a transmitter and receiver and fading distortion with high accuracy.

(Fifth embodiment)

[0091] FIG.14 is a block diagram illustrating a configuration of a reception apparatus according to the fifth embodiment of the present invention. In addition, in the reception apparatus in FIG.14, the components having the function common to that in the reception apparatus illustrated in FIG.6 are given the same symbols as in FIG.6, and the explanations thereof are omitted.

[0092] In the reception apparatus in FIG.14, the functions of frequency offset compensation apparatus 401, distortion estimation apparatus 402 and detection section 403 are different from those of frequency offset compensation apparatus 201, distortion estimation apparatus 202 and detection section 203 of the reception apparatus illustrated in FIG.6.

[0093] Frequency offset compensation apparatus

401 receives the inputted in-phase component and quadrature component of the received quadrature baseband signal and timing signal, and extracts the pilot symbol. Then, frequency offset compensation apparatus 401 calculates the frequency offset amount of the pilot symbol, and estimates the frequency offset amount between data symbols by the interpolation. Then, frequency offset compensation apparatus 401 compensates the frequency offset of the received quadrature baseband signal by inversely rotating the phase of the received quadrature baseband signal by the calculated or estimated frequency offset amount.

[0094] Then, frequency offset compensation apparatus 401 outputs the in-phase component and quadrature component of the received quadrature baseband signal subjected to the frequency offset compensation to distortion estimation apparatus 402 and detection section 403. In addition, the detailed external configuration of frequency offset compensation apparatus 401 is described later.

[0095] Distortion estimation apparatus 402 receives the inputted in-phase component and quadrature component of the received quadrature baseband signal subjected to the frequency offset compensation and timing signal, estimates amplitude variation amounts of the in-phase component and quadrature component of the received quadrature baseband signal as described later, and outputs the estimated results to detection section 403.

[0096] Detection section 403 executes detection processing on the in-phase component and quadrature component of the received quadrature baseband signal subjected to the frequency offset compensation using the amplitude variation amounts of the in-phase component and quadrature component of the received quadrature baseband signal, and outputs a digital signal.

[0097] The internal configuration of frequency offset compensation apparatus 401 of the distortion estimation apparatus according to this embodiment is next explained using a block diagram in FIG.15. In addition, in frequency offset compensation apparatus 401 in FIG.15, the components having the function common to that in the frequency offset compensation apparatus 201 illustrated in FIG.8 are given the same symbols as in FIG.8, and the explanations thereof are omitted.

[0098] In frequency offset compensation apparatus 401, the functions of frequency offset amount calculation section 411 and frequency offset removing section 412 are different from those of frequency offset amount calculation section 213 and frequency offset removing section 214 in frequency offset compensation apparatus 201 illustrated in FIG.8.

[0099] Frequency offset amount calculation section 411 calculates a frequency offset amount using, for example, phase variation amount θ_i and phase variation amount θ_{i-1} , estimates the frequency offset amount between data symbols by the interpolation. Then, frequency offset amount calculation section 411 outputs a

signal indicative of the frequency offset amount to frequency offset removing section 412. In addition, the frequency offset amount is generally obtained by averaging a plurality of phase variation amounts between pilot symbols obtained previously.

[0100] Frequency offset removing section 412 receives the inputted in-phase component and quadrature component of the received quadrature baseband signal, timing signal and the signal indicative of the frequency offset amount, and based on the frequency offset amount, removes the frequency offset amount from the in-phase component and quadrature component of received quadrature baseband signal to compensate. Frequency offset removing section 412 further outputs the in-phase component and quadrature component of the received quadrature baseband signal subjected to the frequency offset compensation to distortion estimation apparatus 402.

[0101] The internal configuration of distortion estimation apparatus 402 according to this embodiment is next explained using a block diagram in FIG.16.

[0102] Distortion estimation apparatus 402 is mainly comprised of in-phase component approximation section 421, and quadrature component approximation section 422.

[0103] In-phase component approximation section 421 receives the inputted in-phase component of the received quadrature baseband signal subjected to the frequency offset compensation and timing signal, extracts the pilot symbol, and calculates the fading distortion amount of the in-phase component of the data symbol approximately. For example, in FIG.3 previously described, fading distortion amount r_k of the in-phase component of data symbol k ($k=1$ to 7) of block j is calculated with the equation (8) described previously.

[0104] Then, in-phase component approximation section 421 outputs a signal indicative of fading distortion amount r_k of the in-phase component of the data symbol calculated to detection section 403.

[0105] Quadrature component approximation section 422 receives the inputted quadrature component of the received quadrature baseband signal subjected to the frequency offset compensation and timing signal, extracts the pilot symbol, and calculates the fading distortion amount of the quadrature component of the data symbol approximately. For example, in FIG.3, fading distortion amount s_k of the quadrature component of symbol k ($k=1$ to 7) of block j of data symbols is calculated with the equation (9) described previously.

[0106] Then, quadrature component approximation section 422 outputs a signal indicative of fading distortion amount s_k of the quadrature component of the data symbol calculated to detection section 403.

[0107] Thus, by compensating the frequency offset of the received quadrature baseband signal, and estimating the fading distortion of the data symbol using the pilot symbol subjected to the frequency offset compensation, it is possible to estimate the frequency offset

between a transmitter and receiver and the fading distortion with high accuracy.

[0108] Then, as illustrated in FIG.14, by installing the above-described frequency offset compensation apparatus and distortion estimation apparatus in a reception apparatus, since it is possible to estimate the frequency offset between a transmitter and receiver and the fading distortion both with high accuracy to detect, it is possible to suppress the deterioration of the bit error rate characteristic.

(Sixth embodiment)

[0109] In the sixth embodiment of the present invention, excellent bit error rate characteristic can be obtained even in the case where a fading variation is large, and a signal with low field strength is received.

[0110] FIG.17 is a block diagram illustrating a configuration of a reception apparatus according to the sixth embodiment of the present invention. In addition, in the reception apparatus in FIG.17, the components having the function common to that in the reception apparatus illustrated in FIG.14 are given the same symbols as in FIG.14, and the explanations thereof are omitted.

[0111] In the reception apparatus in FIG.17, the functions of frequency offset compensation apparatus 501 and detection section 502 are different from those of frequency offset compensation apparatus 401 and detection section 403 of the reception apparatus illustrated in FIG.14. Further, the reception apparatus in FIG.17 has a configuration in which distortion estimation apparatus 402 is eliminated as compared to the reception apparatus illustrated in FIG.14.

[0112] Frequency offset compensation apparatus 501 receives the inputted in-phase component and quadrature component of the received quadrature baseband signal and timing signal, and extracts the pilot symbol. Then, frequency offset compensation apparatus 501 judges whether or not the pilot symbol was detected effectively, calculates a frequency offset amount using only the effectively detected pilot symbol, and compensates the frequency offset amount between data symbols by the interpolation. Then, the frequency offset compensation apparatus 501 compensates the frequency offset amount of the received quadrature baseband signal by inversely rotating the phase of the received quadrature baseband signal by the calculated or estimated frequency offset amount.

[0113] Then, frequency offset compensation apparatus 501 outputs the in-phase component and quadrature component of the received quadrature baseband signal subjected to the frequency offset compensation to detection section 502.

[0114] FIG.18 is a block diagram illustrating an internal configuration of frequency offset compensation apparatus 501. In frequency offset compensation apparatus 501 in FIG.18, the function of frequency offset

amount calculation section 511 is different from that of frequency offset calculation amount section 411 in frequency offset compensation apparatus 401 illustrated in FIG.15. Further, frequency offset compensation apparatus 501 in FIG.18 adopts a configuration in which correlation value calculation section 512 is added as compared to frequency offset compensation apparatus 401 illustrated in FIG.15.

[0115] Correlation value calculation section 512 receives phase variation amount θ_i inputted from phase variation amount calculation section 211 and phase variation amount θ_{i-1} inputted from memory section 212, and calculates the correlation value indicative of the correlation between phase variation amount θ_i and phase variation amount θ_{i-1} to output to frequency offset amount calculation section 511.

[0116] Based on the correlation value, frequency offset amount calculation section 511 judges whether or not there is the correlation between phase variation amount θ_i and phase variation amount θ_{i-1} , in other words, whether or not pilot symbol i was effectively detected.

[0117] Then, frequency offset amount calculation section 511 calculates the frequency offset amount using phase variation amount θ_i when judges that there is the correlation between phase variation amount θ_i and phase variation amount θ_{i-1} , while calculating the frequency offset amount without using phase variation amount θ_i when judges that there is not the correlation between phase variation amount θ_i and phase variation amount θ_{i-1} .

[0118] In addition, it may be possible that frequency offset amount calculation section 511 outputs the correlation result signal to an external apparatus such as a CPU not shown in the figure in the same way as in frequency offset amount calculation section 511.

[0119] Further, it may be possible that correlation value calculation section 512 outputs the correlation value signal to detection section 502, and that detection section 502 judges whether or not there is the correlation between phase variation amount θ_i and phase variation amount θ_{i-1} in the same way as in correlation value calculation section 312.

[0120] Furthermore, it may be possible that correlation value calculation section 512 outputs the correlation value signal to an external apparatus such as a CPU not shown in the figure, and that the external apparatus such as the CPU judges whether or not there is the correlation between phase variation amount θ_i and phase variation amount θ_{i-1} , and based on the judgement result, performs an judgement of data effectiveness and judgment of channel switch such as diversity and handover, in the same way as in correlation value calculation section 312. In this case, phase variation amount calculation section 211, memory section 212 and correlation value calculation section 512 function as the control apparatus to calculate the correlation value.

[0121] Thus, by judging whether or not the pilot

symbol was detected effectively, and calculating the frequency offset amount of the received quadrature baseband signal using only the effectively detected pilot symbol, it is possible to obtain excellent bit error rate characteristic even in the case where the fading variation is large, and the signal with low received field strength is received.

(Seventh embodiment)

[0122] The seventh embodiment of the present invention is obtained by combining the fifth embodiment and sixth embodiment, and in this embodiment in the case where a reception apparatus has a frequency offset compensation apparatus and distortion estimation apparatus installed therein, excellent bit error rate characteristic can be obtained even in the case where a fading variation is large, and a signal with low field strength is received.

[0123] FIG. 19 is a block diagram illustrating a configuration of a reception apparatus according to the seventh embodiment of the present invention. In addition, in the reception apparatus in FIG. 19, the components having the function common to that in the reception apparatus illustrated in FIG. 14 are given the same symbols as in FIG. 14, and the explanations thereof are omitted.

[0124] The reception apparatus in FIG. 19 has frequency offset compensation apparatus 501 illustrated in FIG. 17 installed therein, instead of frequency offset compensation apparatus 401, as compared to the reception apparatus illustrated in FIG. 14.

[0125] Frequency offset compensation apparatus 501 receives the inputted in-phase component and quadrature component of the received quadrature baseband signal and timing signal, and extracts the pilot symbol. Then, frequency offset compensation apparatus 501 judges whether or not the pilot symbol was detected effectively, calculates the frequency offset amount using only the effectively detected pilot symbol, and estimates the frequency offset amount between data symbols by the interpolation. Then, frequency offset compensation apparatus 501 compensates the frequency offset amount of the received quadrature baseband signal by inversely rotating the phase of the received quadrature baseband signal by the calculated or estimated frequency offset amount.

[0126] Then, frequency offset compensation apparatus 501 outputs the in-phase component and quadrature component of the received quadrature baseband signal subjected to the frequency offset compensation to distortion estimation apparatus 402 and detection section 403.

[0127] Distortion estimation apparatus 402 receives the inputted in-phase component and quadrature component of the received quadrature baseband signal subjected to the frequency offset compensation and timing signal, estimates amplitude variation amounts of the in-

phase component and quadrature component of the received quadrature baseband signal, and outputs the estimated results to detection section 403.

[0128] Detection section 403 executes detection processing on the in-phase component and quadrature component of the received quadrature baseband signal subjected to the frequency offset compensation using the amplitude variation amounts of the in-phase component and quadrature component of the received quadrature baseband signal, and outputs a digital signal.

[0129] Thus, by judging whether or not the pilot symbol was detected effectively, calculating the frequency offset amount of the received quadrature baseband signal using only the effectively detected pilot symbol to compensate the frequency offset of the received quadrature baseband signal, and estimating the fading distortion of the data symbol using the pilot symbol subjected to the frequency offset compensation, it is possible to obtain excellent bit error rate characteristic even in the case where the fading variation is large, and the signal with low received field strength is received, and further to estimate the frequency offset between a transmitter and receiver and fading distortion with high accuracy.

[0130] In addition, the explanation is performed using the single carrier system as the communication system in each embodiment described above. However, in the present invention, the communication system is not limited, and it may be possible, in the case where an orthogonal frequency division multiplexing system is used, to estimate the frequency offset and fading distortion with high accuracy in demodulating a signal with each subcarrier, and to suppress the deterioration of bit error rate characteristic due to the frequency offset between a transmitter and receiver and fading distortion.

[0131] Further, in the case where a code division multiple access system is used, in the same way as in the single carrier system, it may be possible to estimate the frequency offset and fading distortion with high accuracy after despreading, and to suppress the deterioration of bit error rate characteristic due to the frequency offset between a transmitter and receiver and fading distortion.

[0132] In addition, the present invention is not limited in frame structure of a received signal, and is achieved in other cases besides that illustrated in FIG. 3. Further, although each embodiment described above explains about the case where the first-order Gaussian polynomial approximation is used as a polynomial approximation, the present invention is not limited to the above case, and the same effects may be obtained using other approximation systems such as least square method.

[0133] The present invention is not limited to the above described embodiments, and various variations and modifications may be possible without departing from the scope of the present invention.

[0134] This application is based on the Japanese Patent Applications No.HEI11-034038 filed on February 12, 1999, No.HEI11-041227 filed on February 19, 1999, and No.HEI11-044390 filed on February 23, 1999, entire contents of which are expressly incorporated by reference herein. 5

Claims

1. A distortion estimation apparatus comprising: 10
 amplitude variation amount estimator (111,112,113) for estimating an amplitude variation amount of a data symbol based on an amplitude variation amount of a known symbol; 15
 and
 phase variation amount estimator (114) for estimating a phase variation amount of said data symbol based on a phase variation amount of said known symbol. 20
2. The distortion estimation apparatus according to claim 1, wherein said amplitude variation amount estimator (111,112,113) estimates the amplitude variation amount of said data symbol by an interpolation based on the amplitude variation amount of said known symbol. 25
3. The distortion estimation apparatus according to claim 1, wherein said phase variation amount estimator (114) estimates the phase variation amount of said data symbol by an interpolation based on the phase variation amount of said known symbol. 30
4. A reception apparatus having a distortion estimation apparatus, said distortion estimation apparatus comprising: 35
 amplitude variation amount estimator for estimating an amplitude variation amount of a data symbol based on an amplitude variation amount of a known symbol; and
 phase variation amount estimator for estimating a phase variation amount of said data symbol based on a phase variation amount of said known symbol. 40
 45
5. A control apparatus comprising: 50
 phase variation amount calculator (211) for calculating a phase variation amount of a known symbol; and
 correlation value calculator (312) for calculating a correlation value that is a difference between the phase variation amount of the known symbol received currently and the phase variation amount of the known symbol received previously. 55

6. A frequency offset compensation apparatus comprising:

correlation value calculator (211,312) for calculating a correlation value that is a difference between a phase variation amount of a known symbol received currently and the phase variation amount of the known symbol received previously;
 frequency offset amount calculator (213) for calculating a first frequency offset amount of said known symbol received currently using a plurality of known symbols satisfying a condition predetermined based on said correlation value; and
 frequency offset remover (214) for removing a frequency offset of said known symbol received currently using said first frequency offset amount.

7. The frequency offset compensation apparatus according to claim 6, wherein said frequency offset amount compensator (213) calculates said first frequency offset amount using phase variation amounts of a plurality of known symbols each with the correlation value equal to or less than a predetermined threshold.
8. The frequency offset compensation apparatus according to claim 6, wherein said frequency offset remover (214) estimates a second frequency offset amount of a data symbol based on a frequency offset amount of the known symbol, and using the second frequency offset amount, removes a frequency offset of said data symbol.
9. The frequency offset compensation apparatus according to claim 8, wherein said frequency offset remover (214) estimates the second frequency offset amount by an interpolation based on the frequency offset amount of the known symbol.
10. A reception apparatus comprising:
 a frequency offset compensation apparatus (201) which removes a frequency offset of a known symbol; and
 a distortion estimation apparatus (202) which estimates an amplitude variation amount of a data symbol based on an amplitude variation amount of a known symbol from which the frequency offset is removed.
11. The reception apparatus according to claim 10, wherein said frequency offset compensation apparatus (201) comprising:

correlation value calculator (211) for calculating

a correlation value that is a difference between a phase variation amount of a known symbol received currently and a phase variation amount of a known symbol received previously; frequency offset amount calculator (213) for calculating a first frequency offset amount of said known symbol received currently using a plurality of known symbols satisfying a condition predetermined based on said correlation value; frequency offset remover (214) for removing a frequency offset of said known symbol received currently using said first frequency offset amount.

12. A reception apparatus comprising:

a frequency offset compensation apparatus (201) which estimates a frequency offset amount of a data symbol based on a frequency offset amount of a known symbol, and removes a frequency offset of said known symbol and a frequency offset of said data symbol using said frequency offset amount; and a distortion estimation apparatus (202) which estimates an amplitude variation amount of said data symbol from which the frequency offset is removed based on the amplitude variation amount of said known symbol from which the frequency offset is removed.

13. The reception apparatus according to claim 12, wherein said frequency offset compensation apparatus (201) comprising:

correlation value calculator (211) for calculating a correlation value that is a difference between a phase variation amount of a known symbol received currently and a phase variation amount of a known symbol received previously; frequency offset amount calculator (213) for calculating a first frequency offset amount of said known symbol received currently using a plurality of known symbols satisfying a condition predetermined based on said correlation value; frequency offset remover (214) for removing a frequency offset of said known symbol received currently using said first frequency offset amount, estimating a second frequency offset amount of the data symbol based on the frequency offset amount of the known symbol, and removing the frequency offset of said data symbol using the second frequency offset amount.

14. A distortion estimation method comprising:

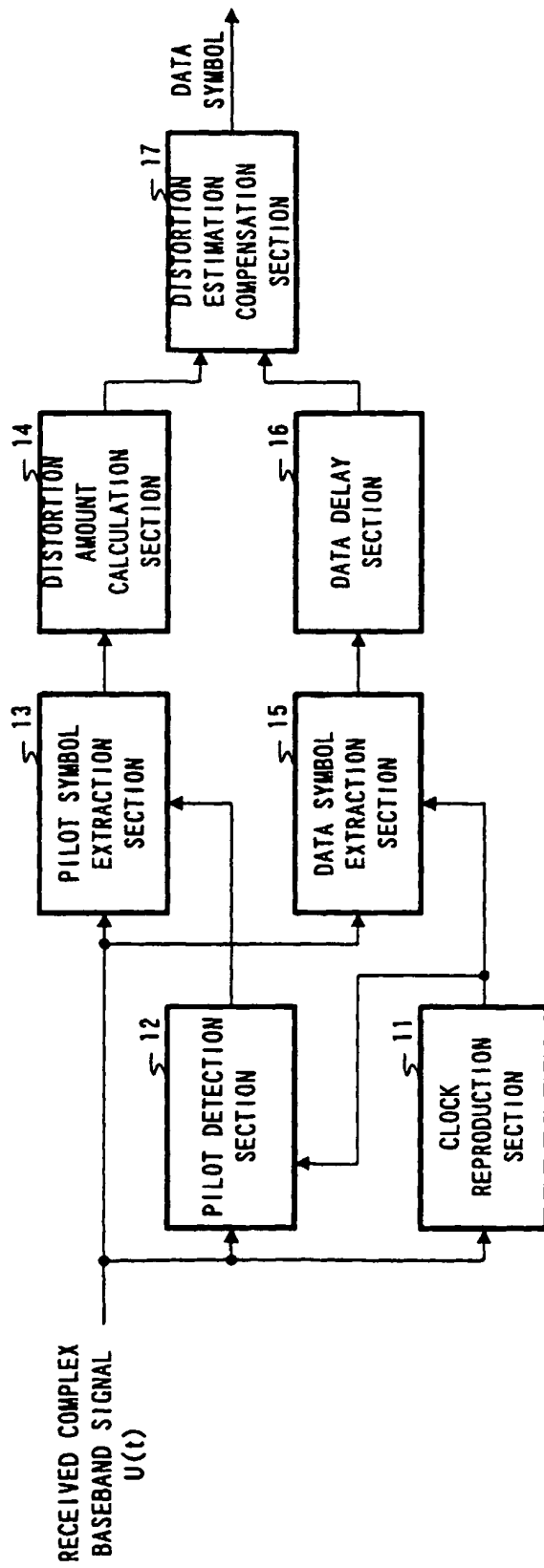
estimating an amplitude variation amount of a data symbol based on an amplitude variation amount of a known symbol; and estimating a phase variation amount of said data symbol based on a phase variation amount of said known symbol.

15. A frequency offset compensation method comprising:

calculating a correlation value that is a difference between a phase variation amount of a known symbol received currently and the phase variation amount of the known symbol received previously; calculating a first frequency offset amount of said known symbol received currently using a plurality of known symbols satisfying a condition predetermined based on said correlation value; and removing a frequency offset of said known symbol received currently using said first frequency offset amount.

16. The frequency offset compensation method according to claim 15, further comprising:

estimating a second frequency offset amount of a data symbol based on a frequency offset amount of the known symbol; and removing a frequency offset of said data symbol using the second frequency offset amount.



PRIOR ART
FIG. 1

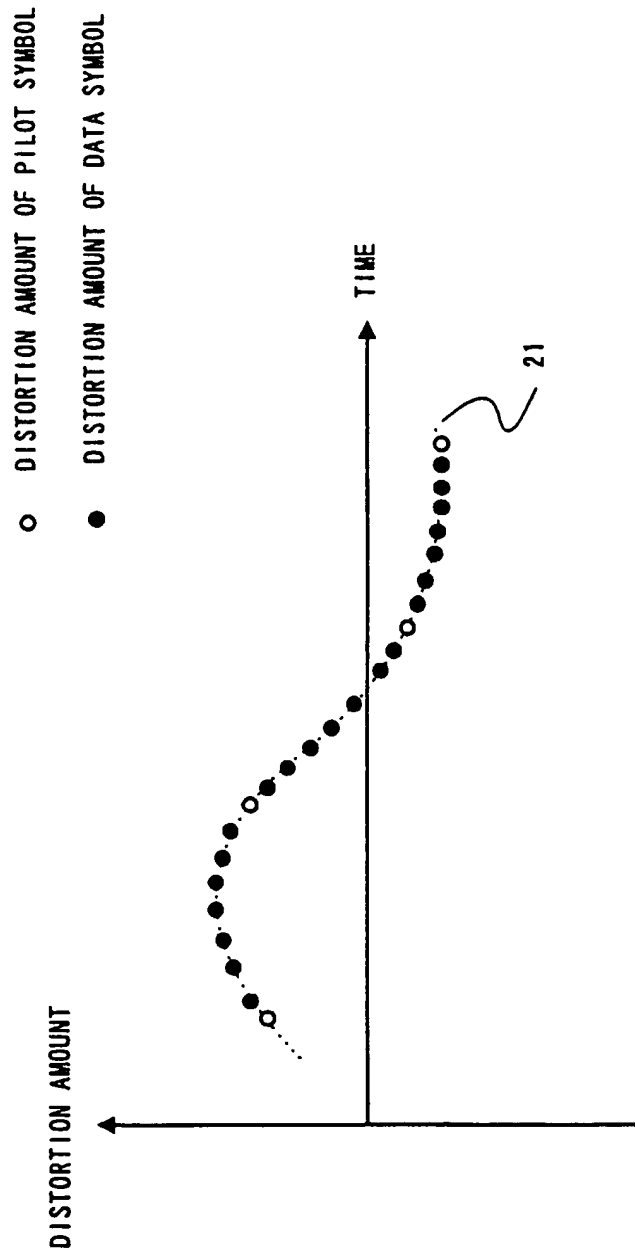


FIG. 2

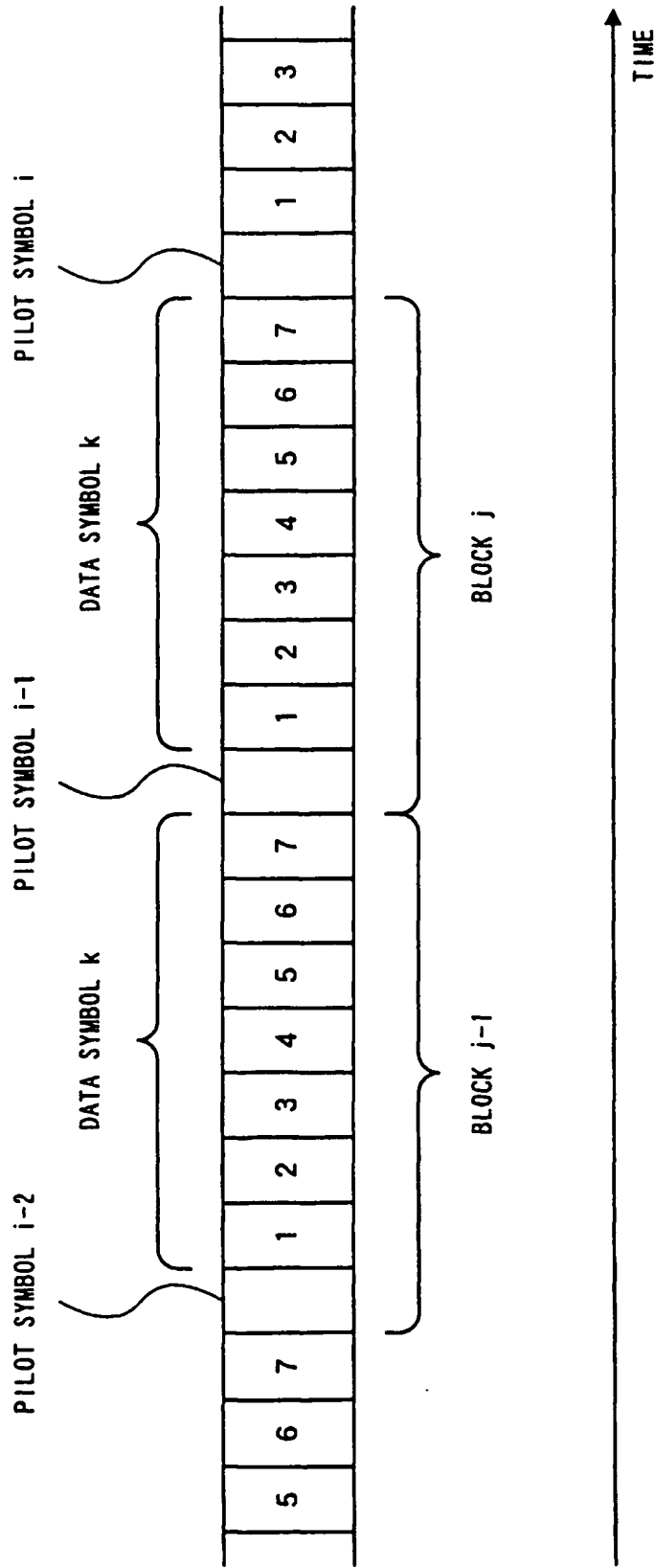


FIG. 3

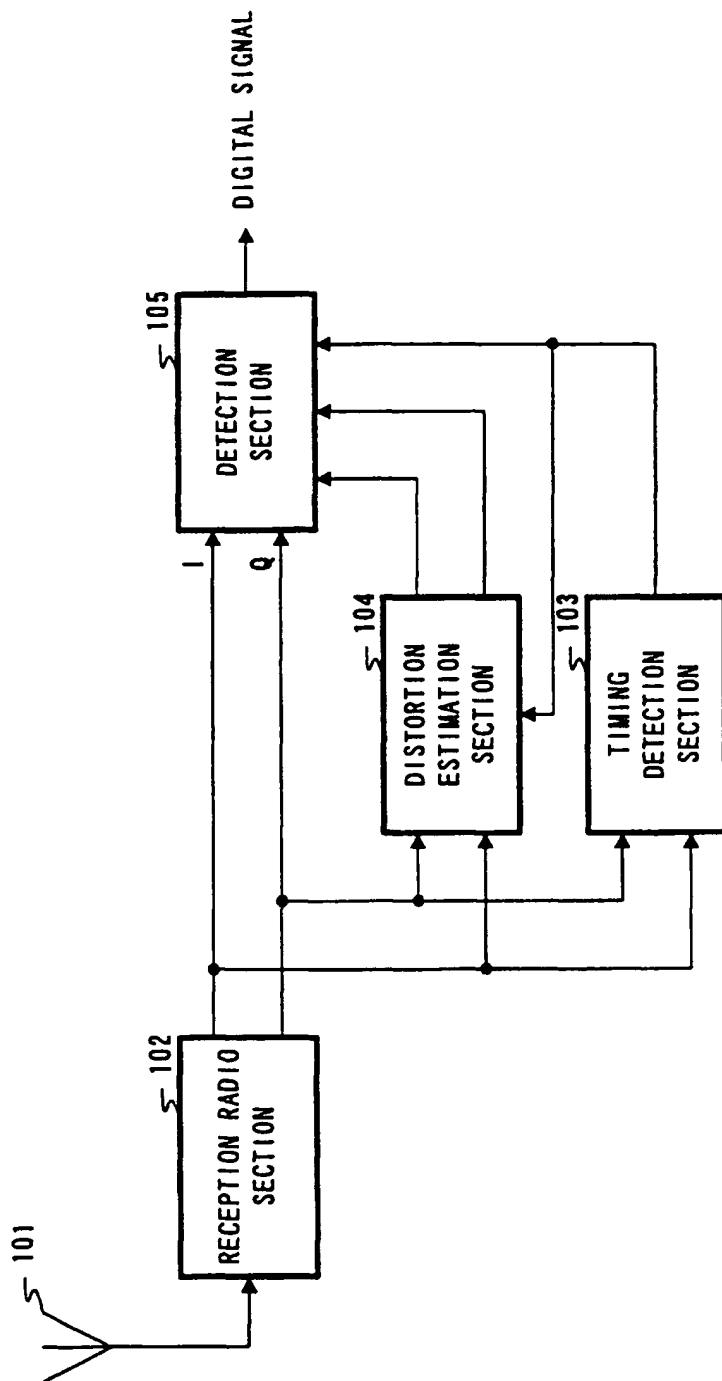


FIG. 4

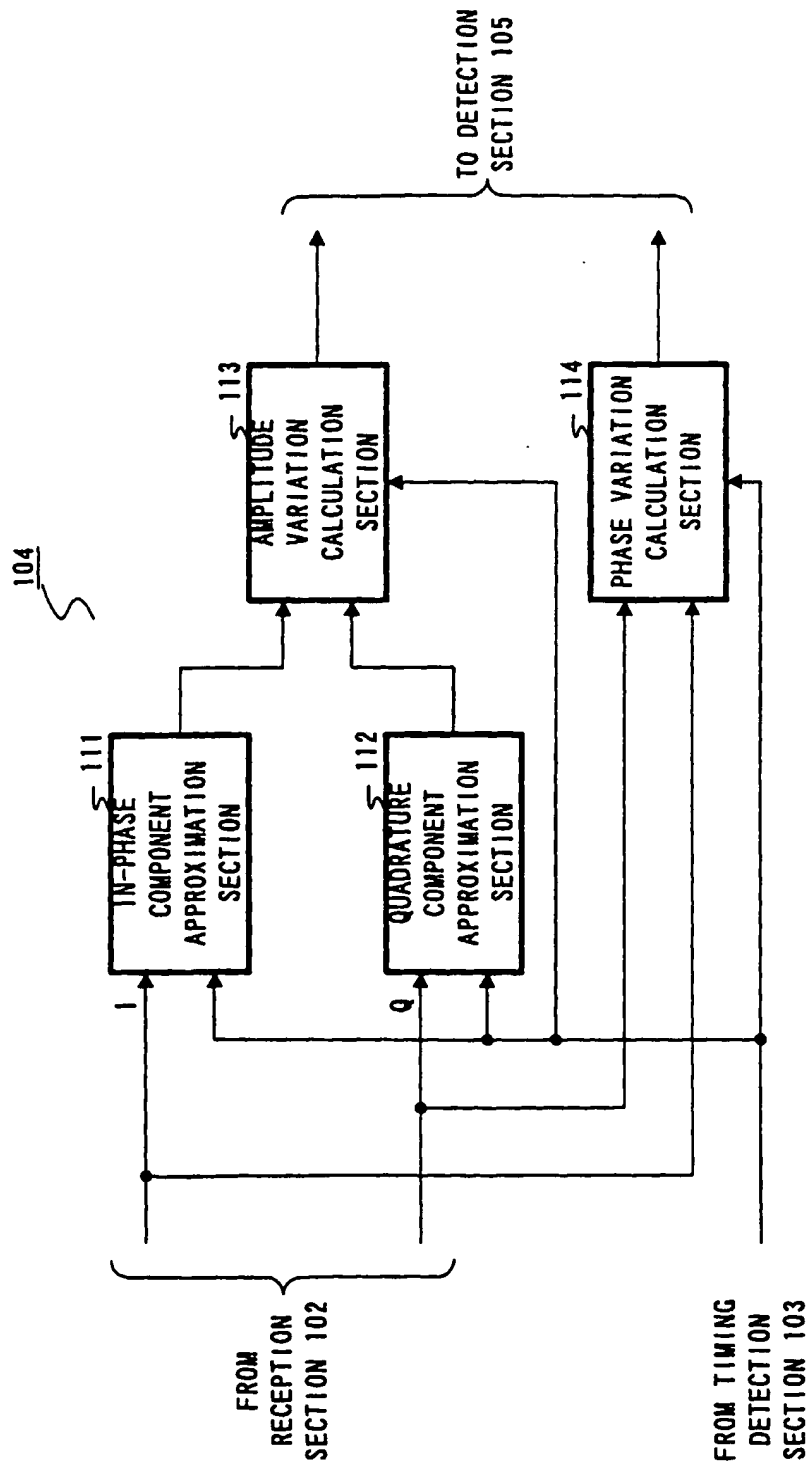


FIG. 5

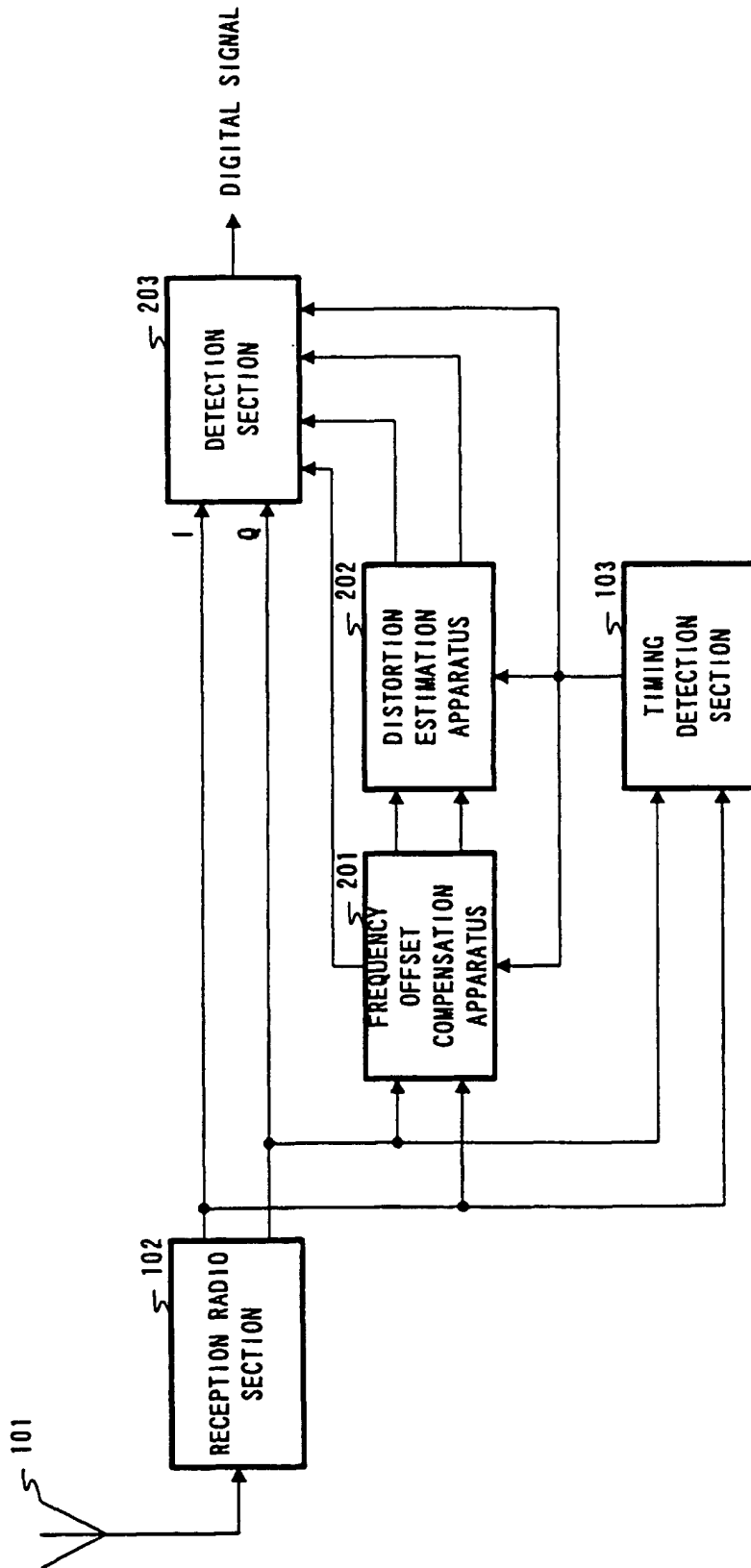


FIG. 6

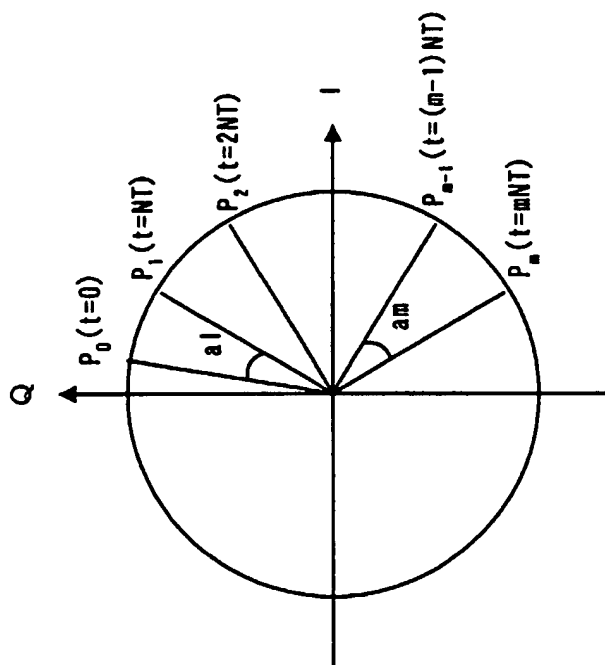


FIG. 7

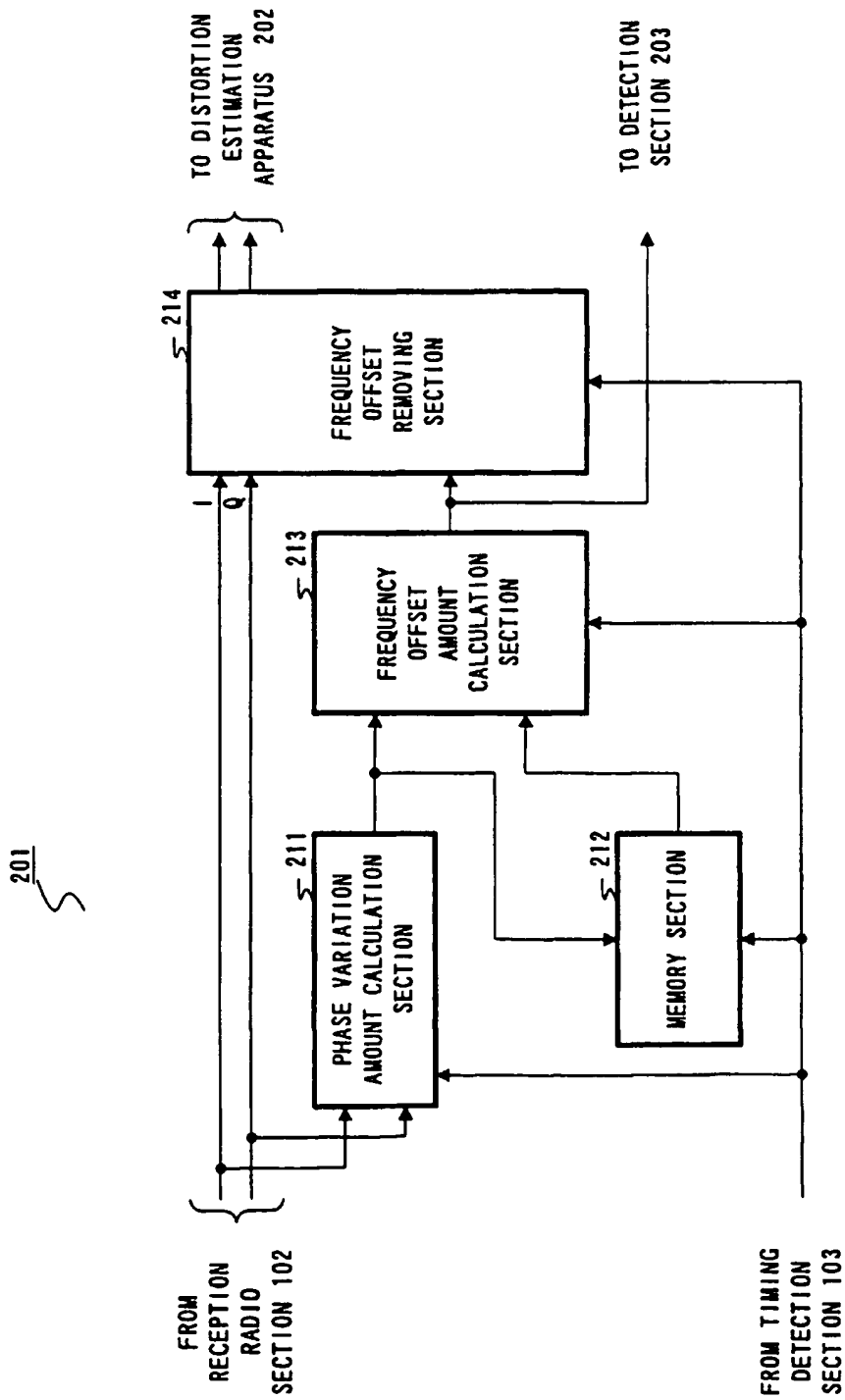


FIG. 8

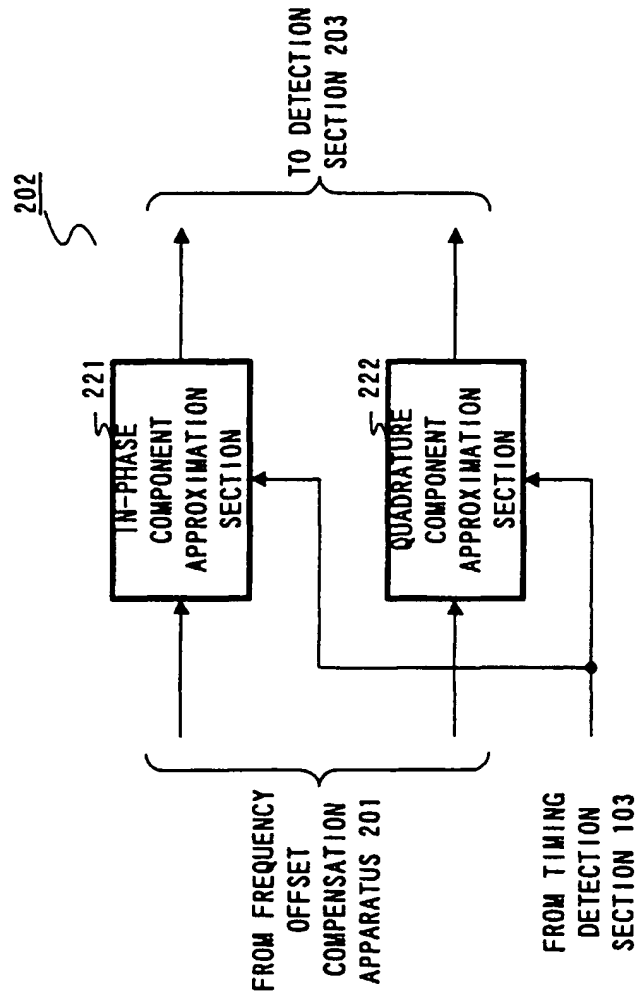


FIG. 9

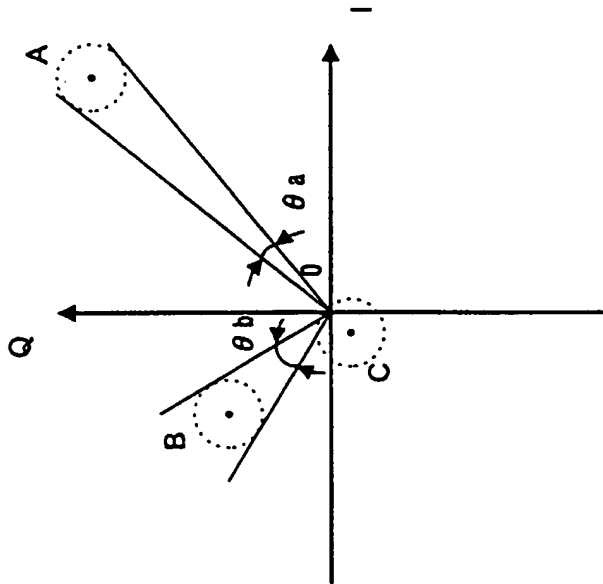


FIG. 10

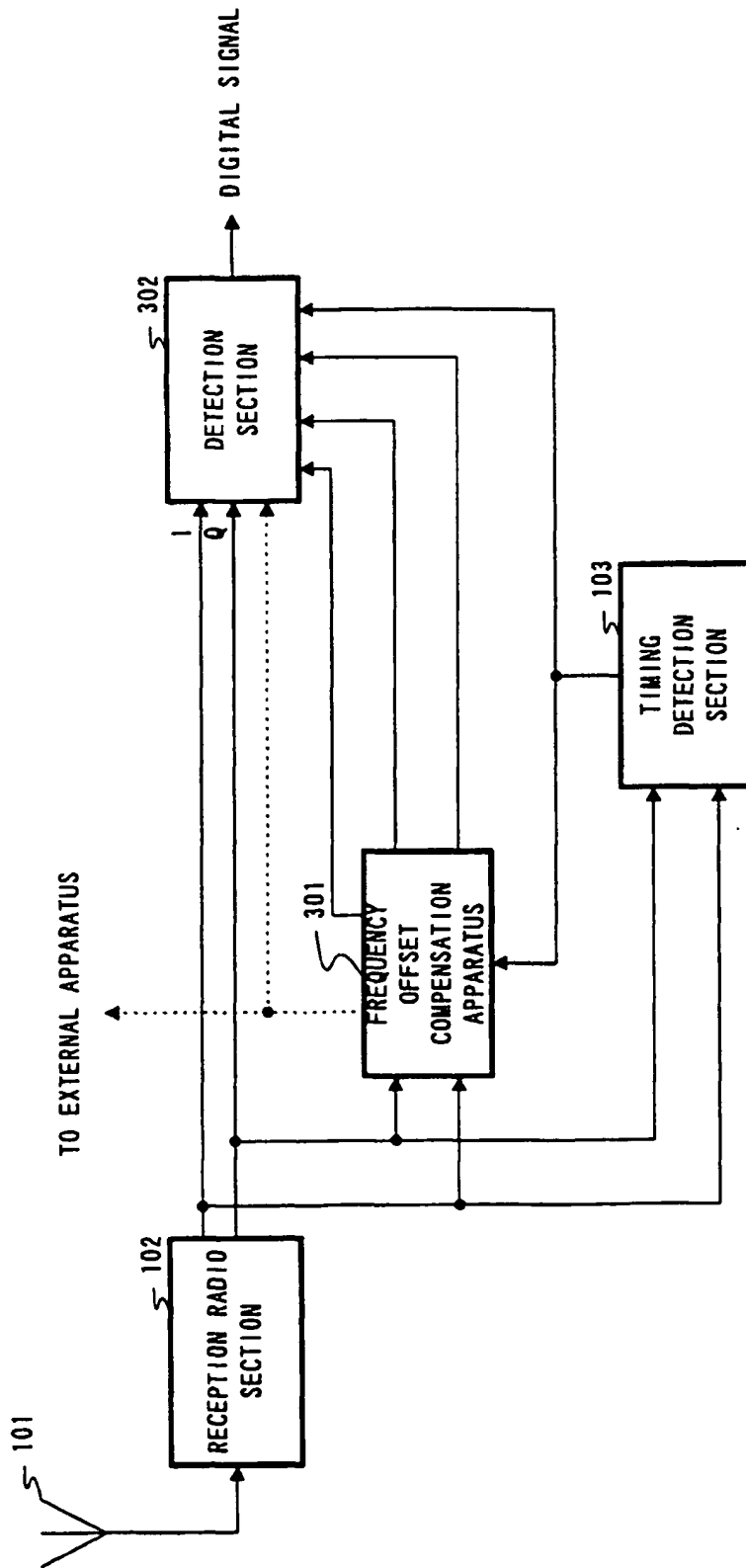


FIG. 11

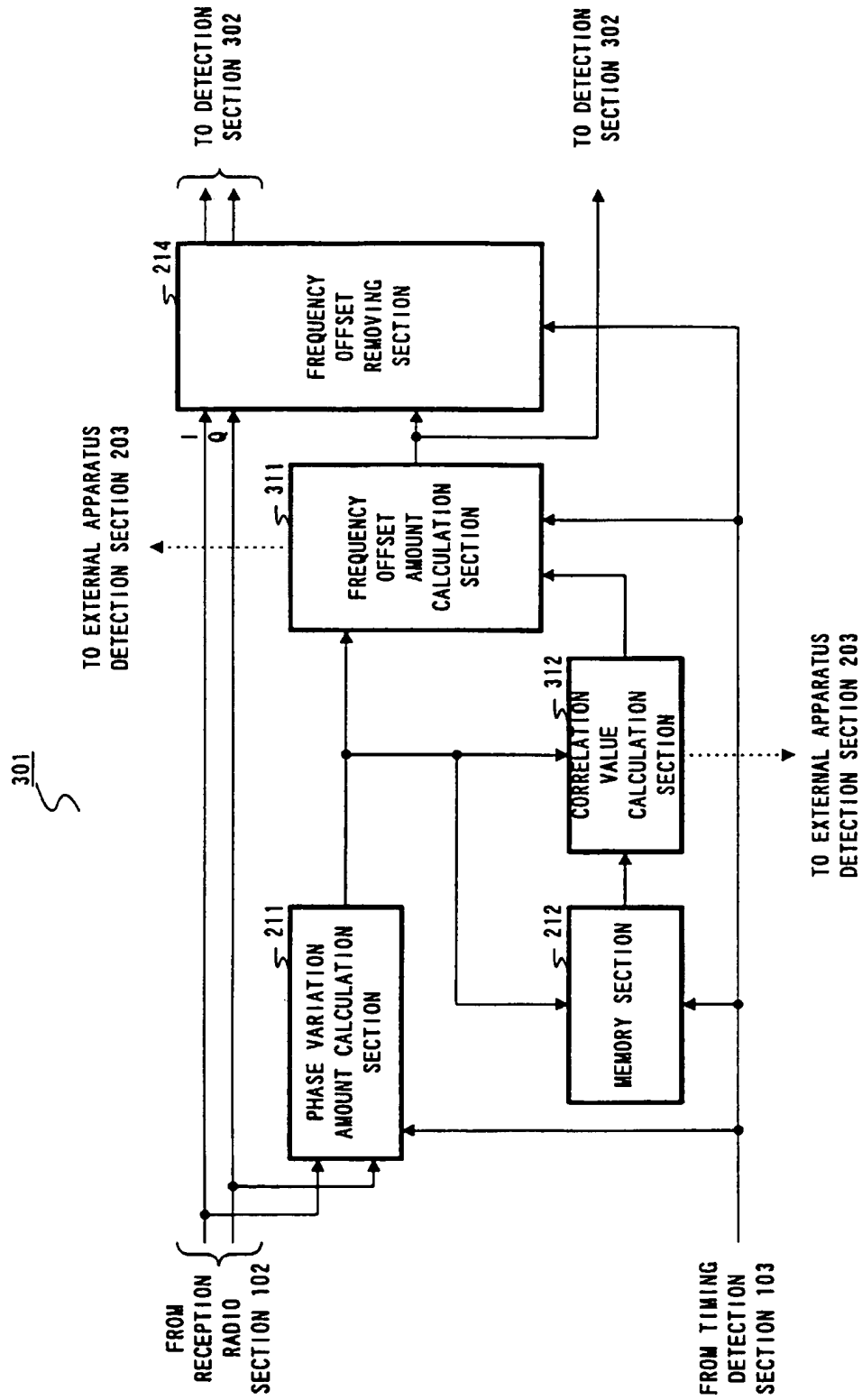


FIG. 12

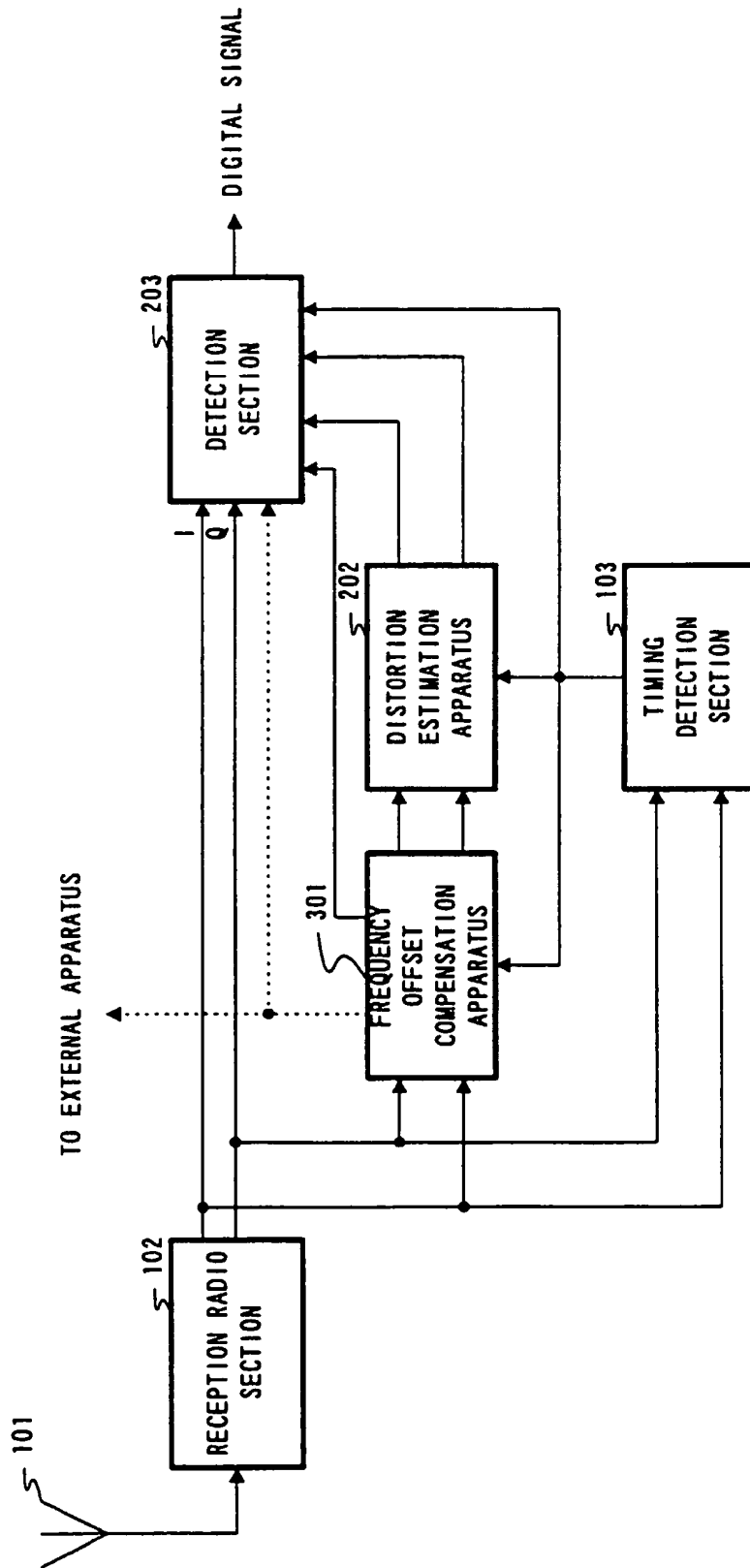


FIG. 13

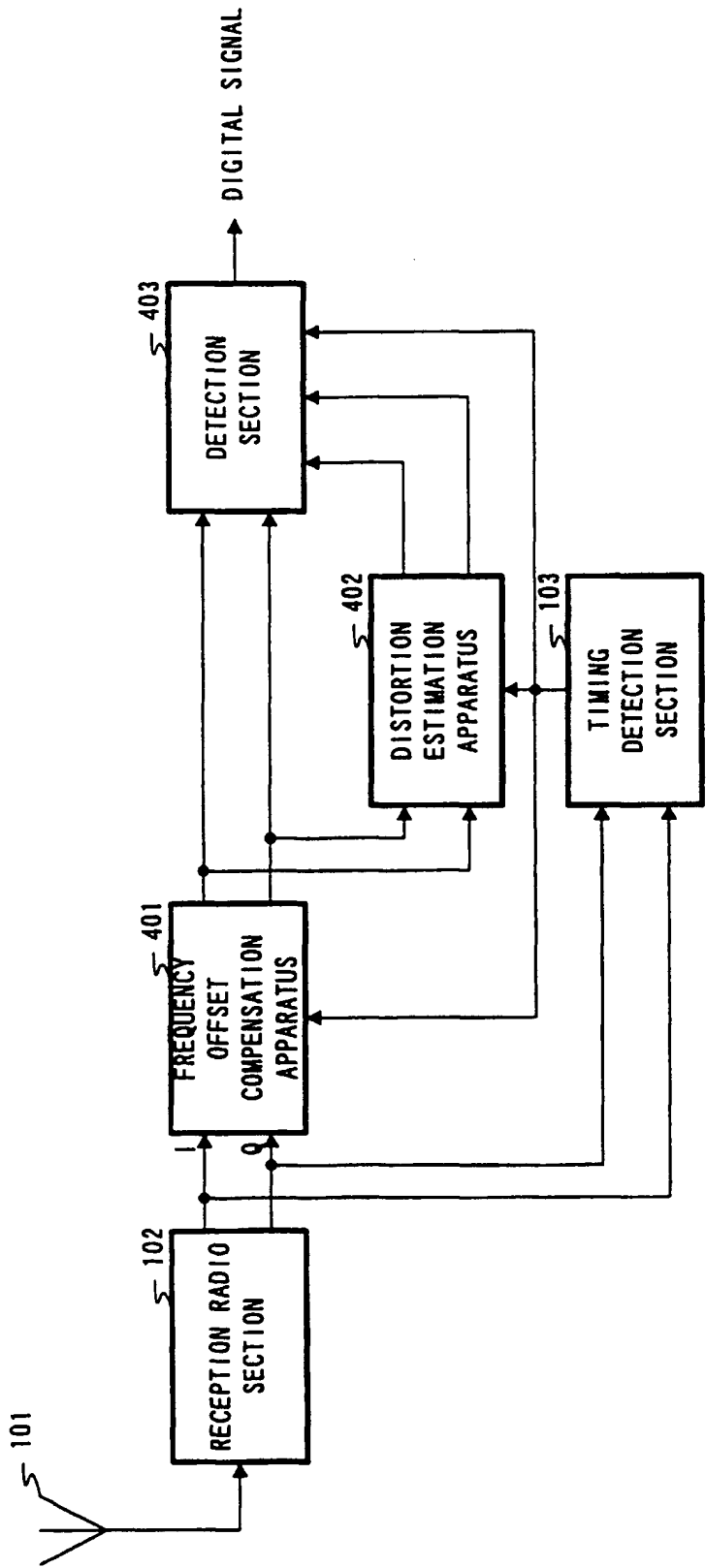


FIG. 14

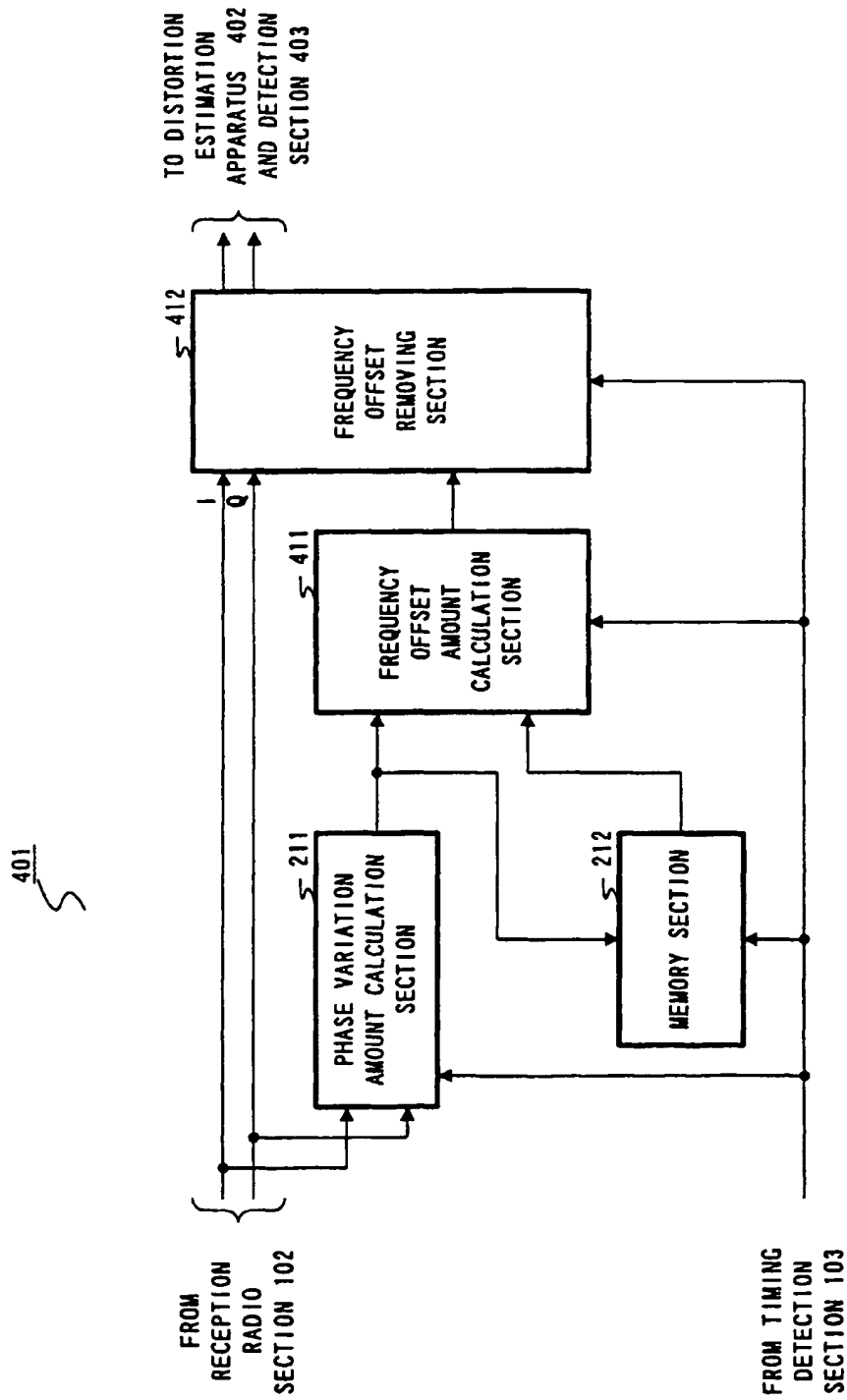


FIG. 15

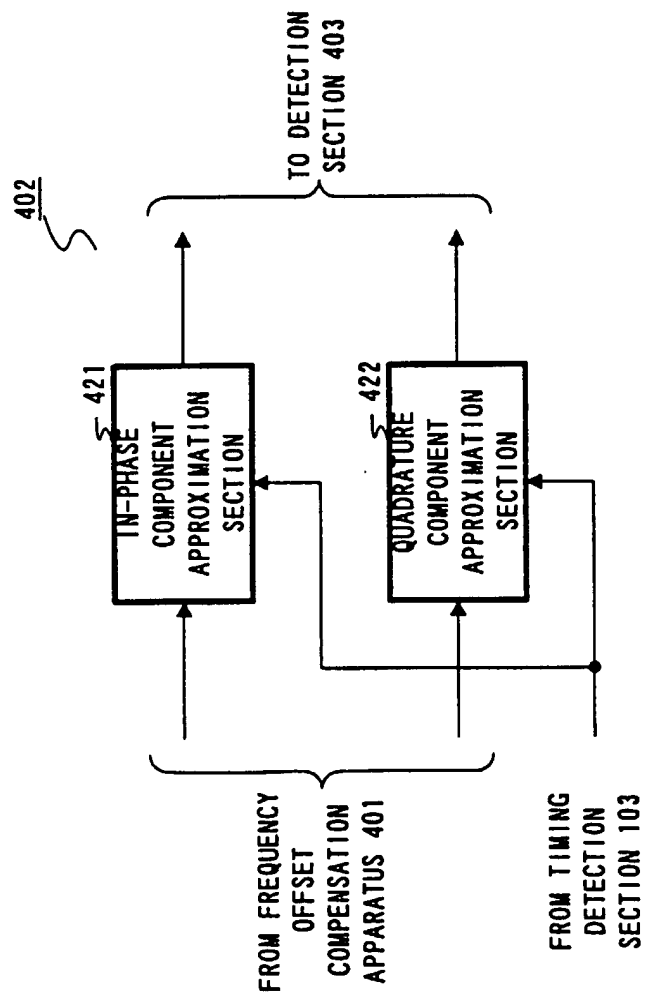


FIG. 16

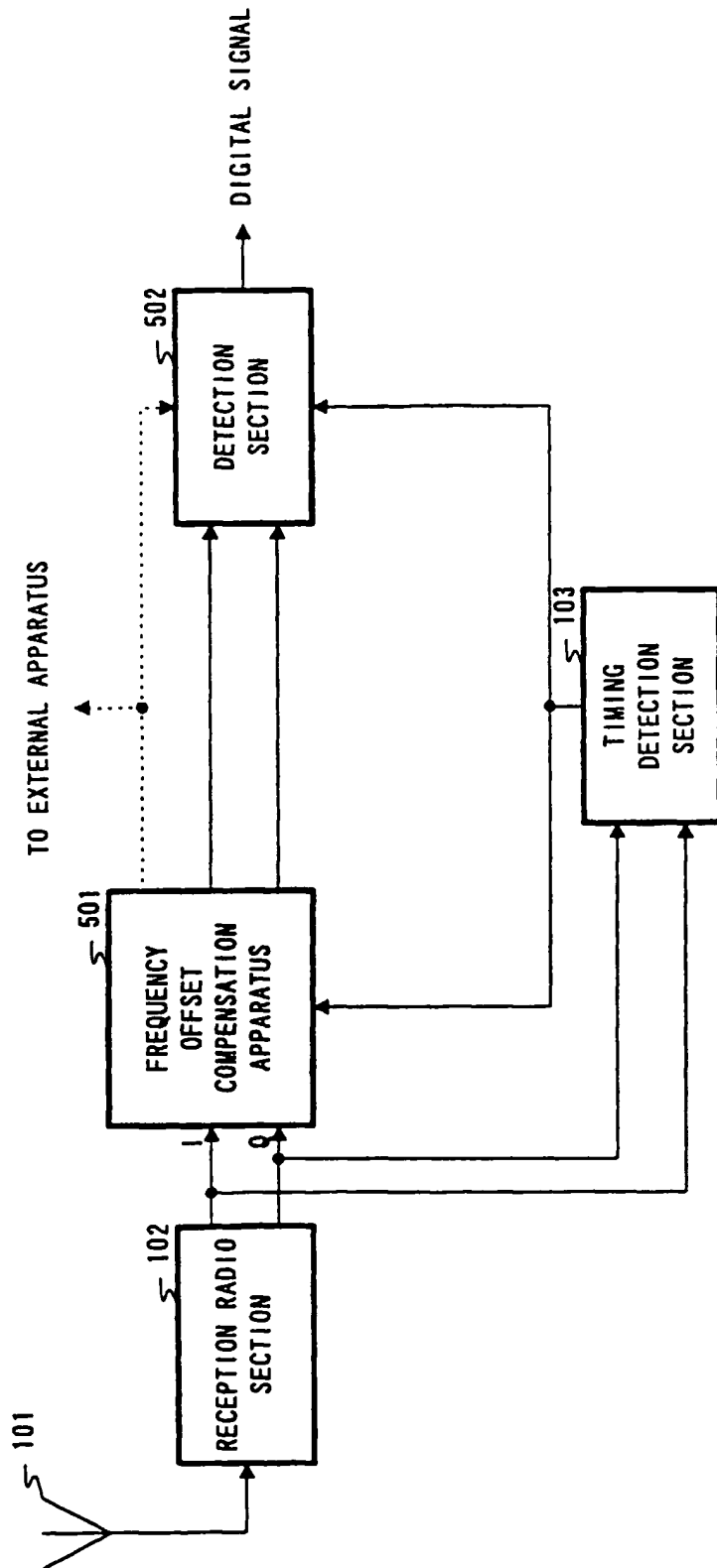


FIG. 17

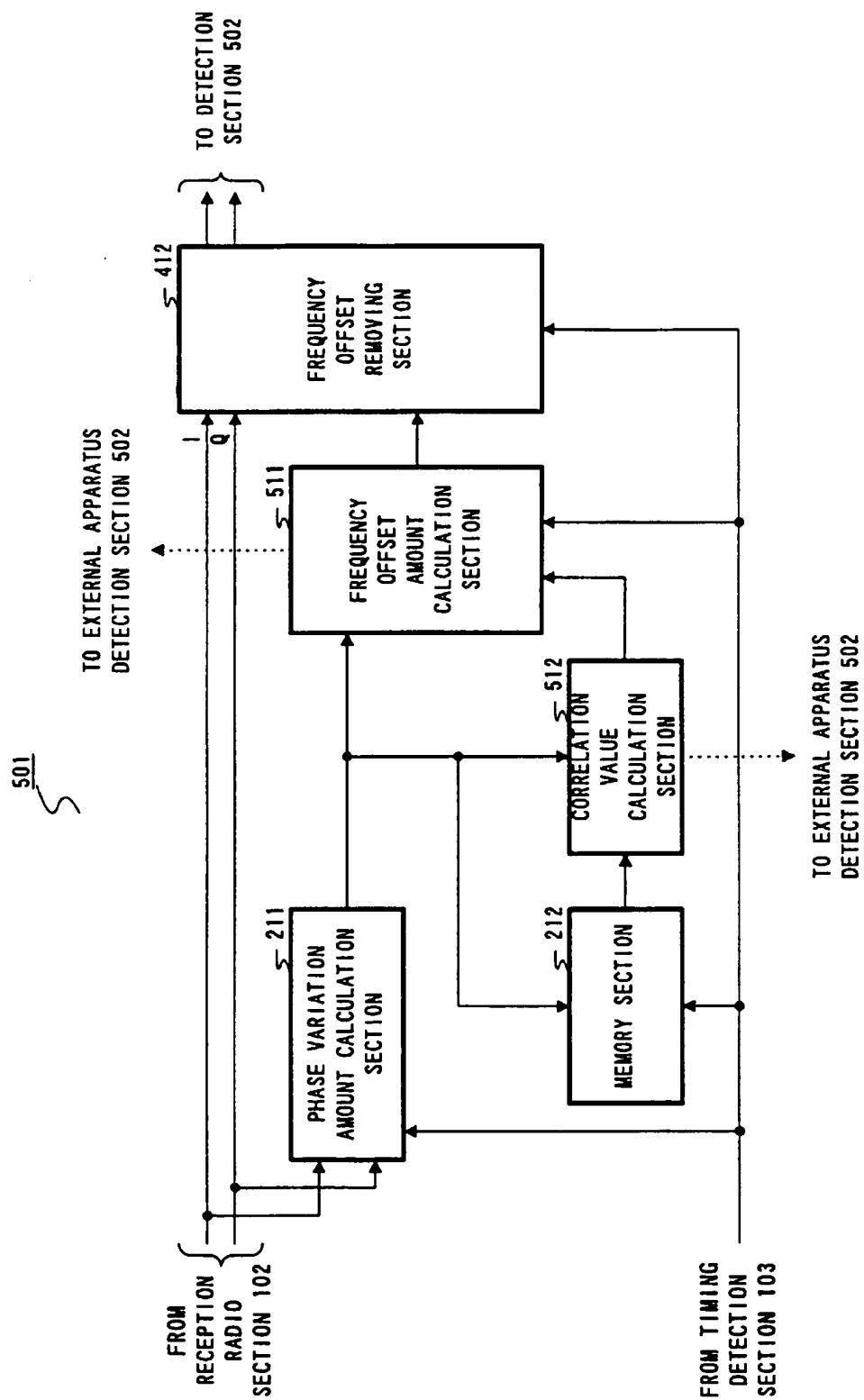


FIG. 18

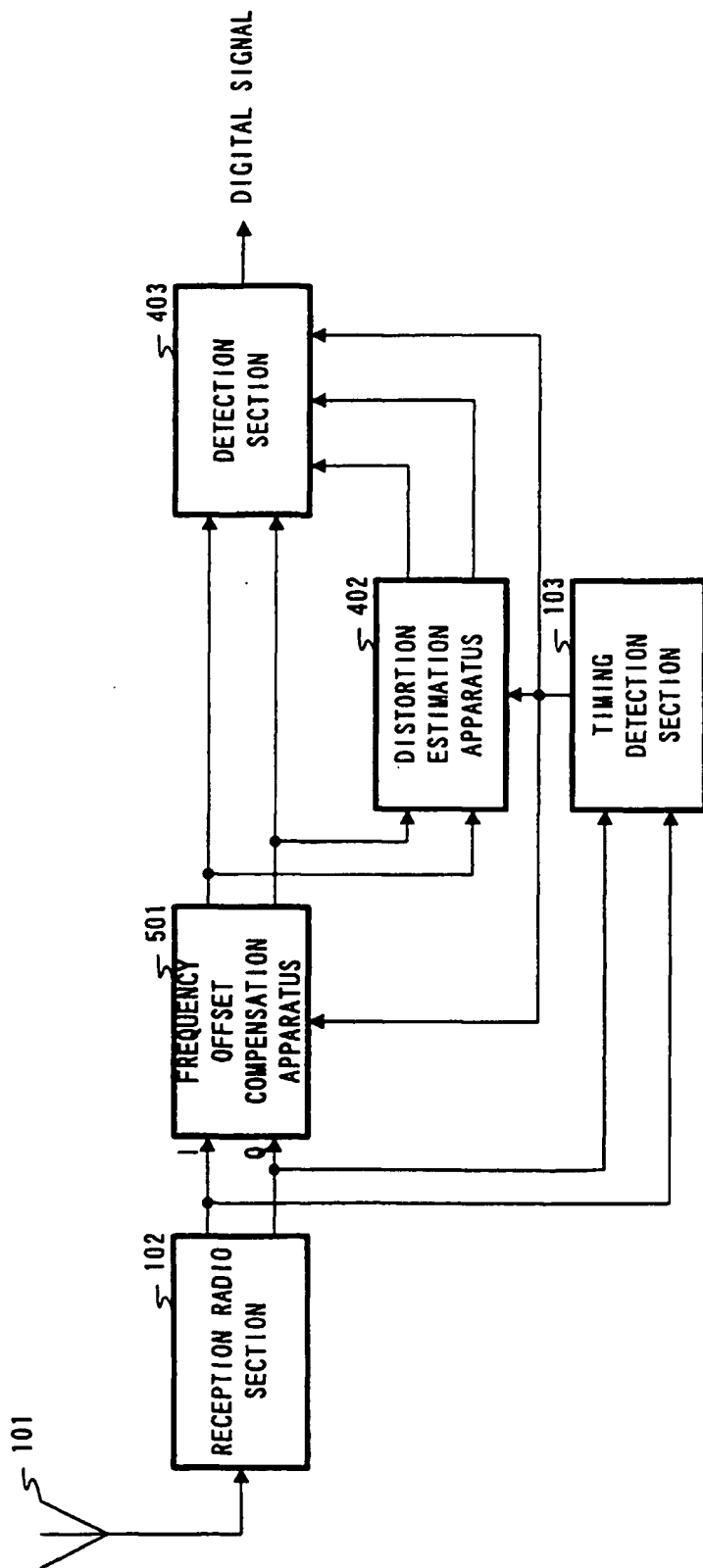


FIG. 19